**Figure 1**

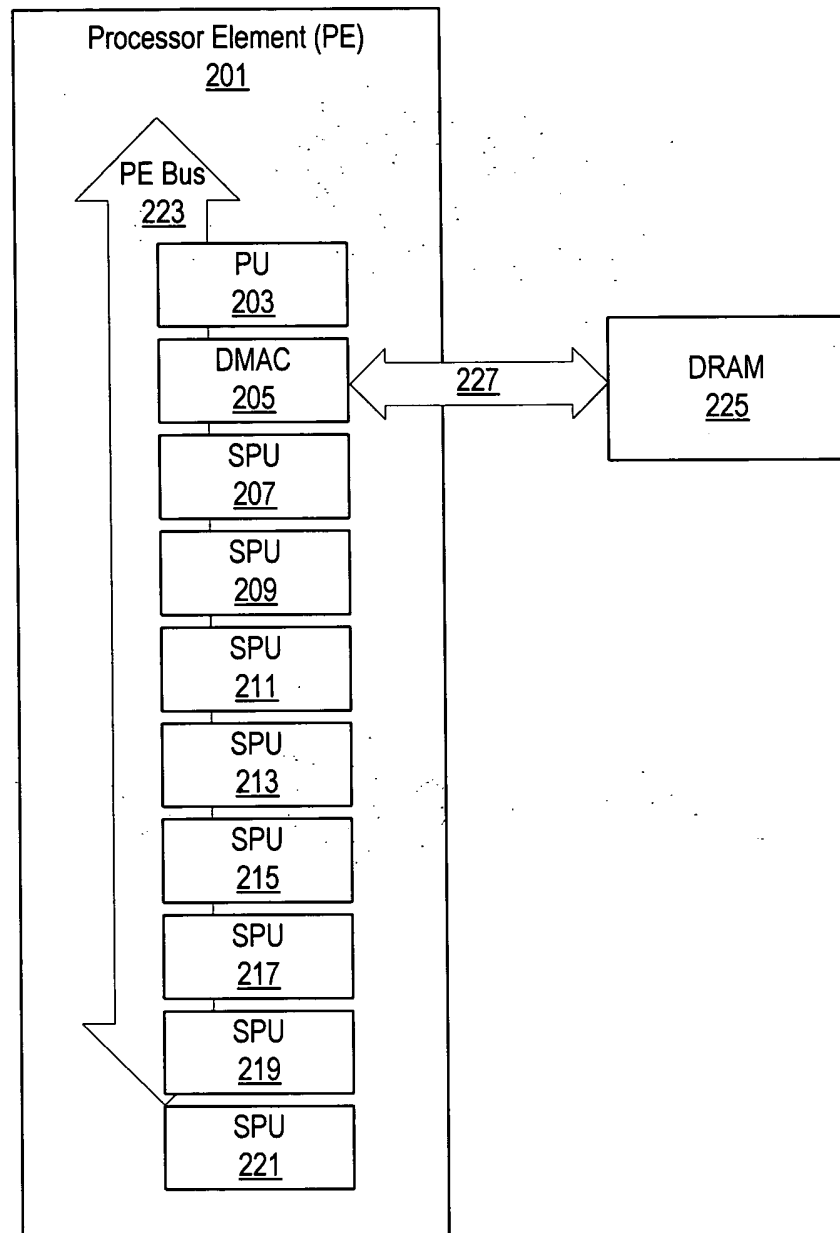
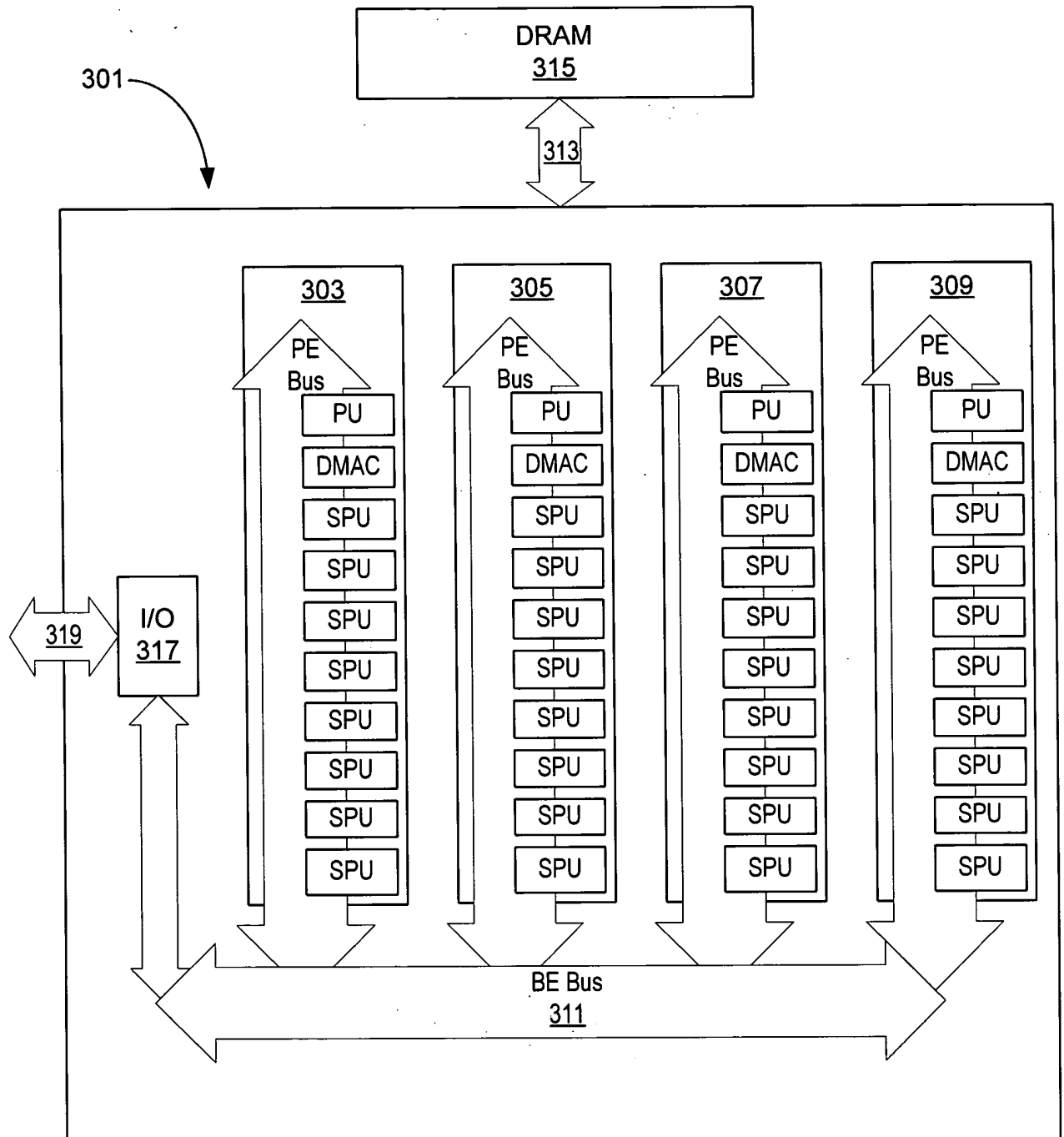
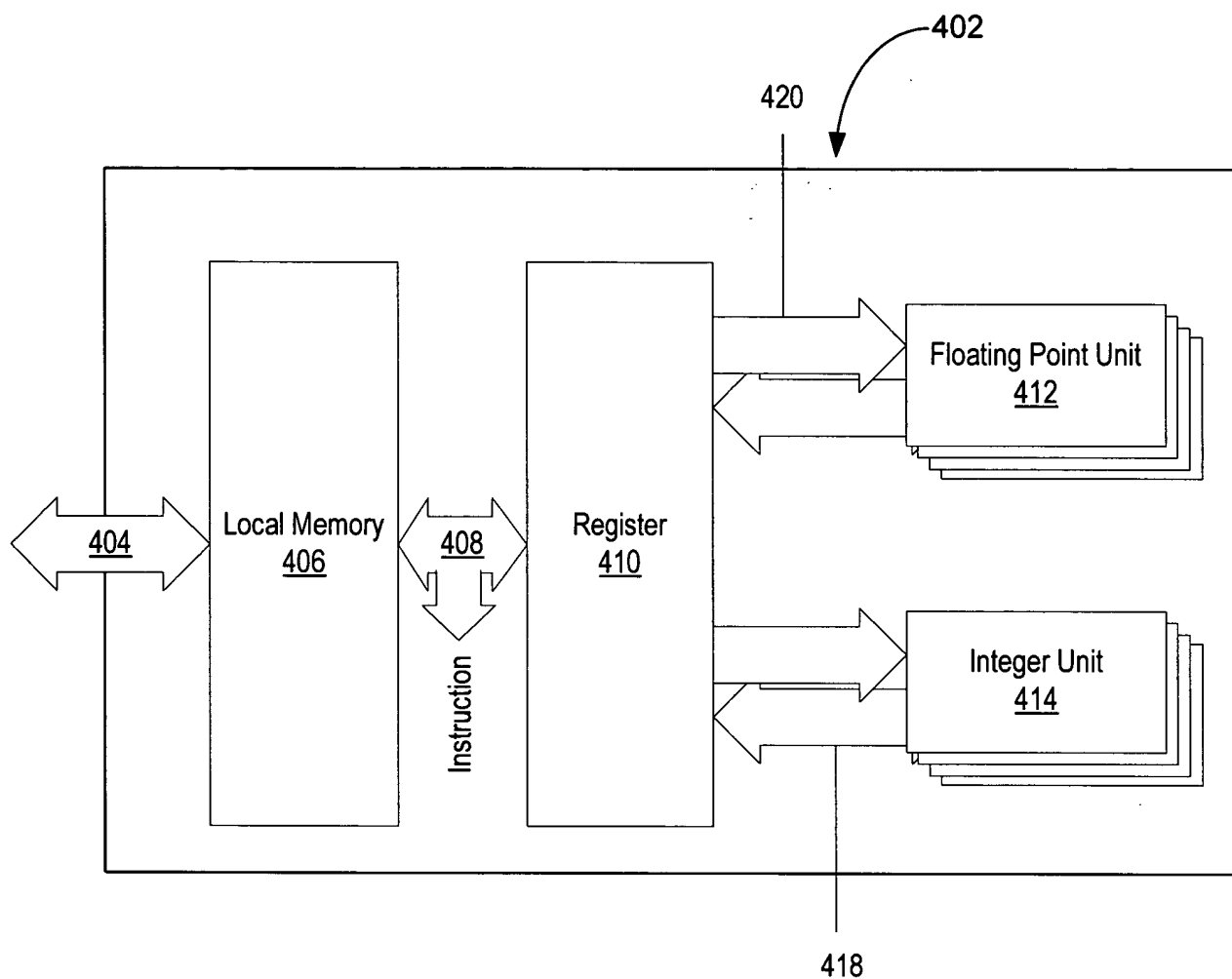


Figure 2

3 / 49

**Figure 3**

**Figure 4**

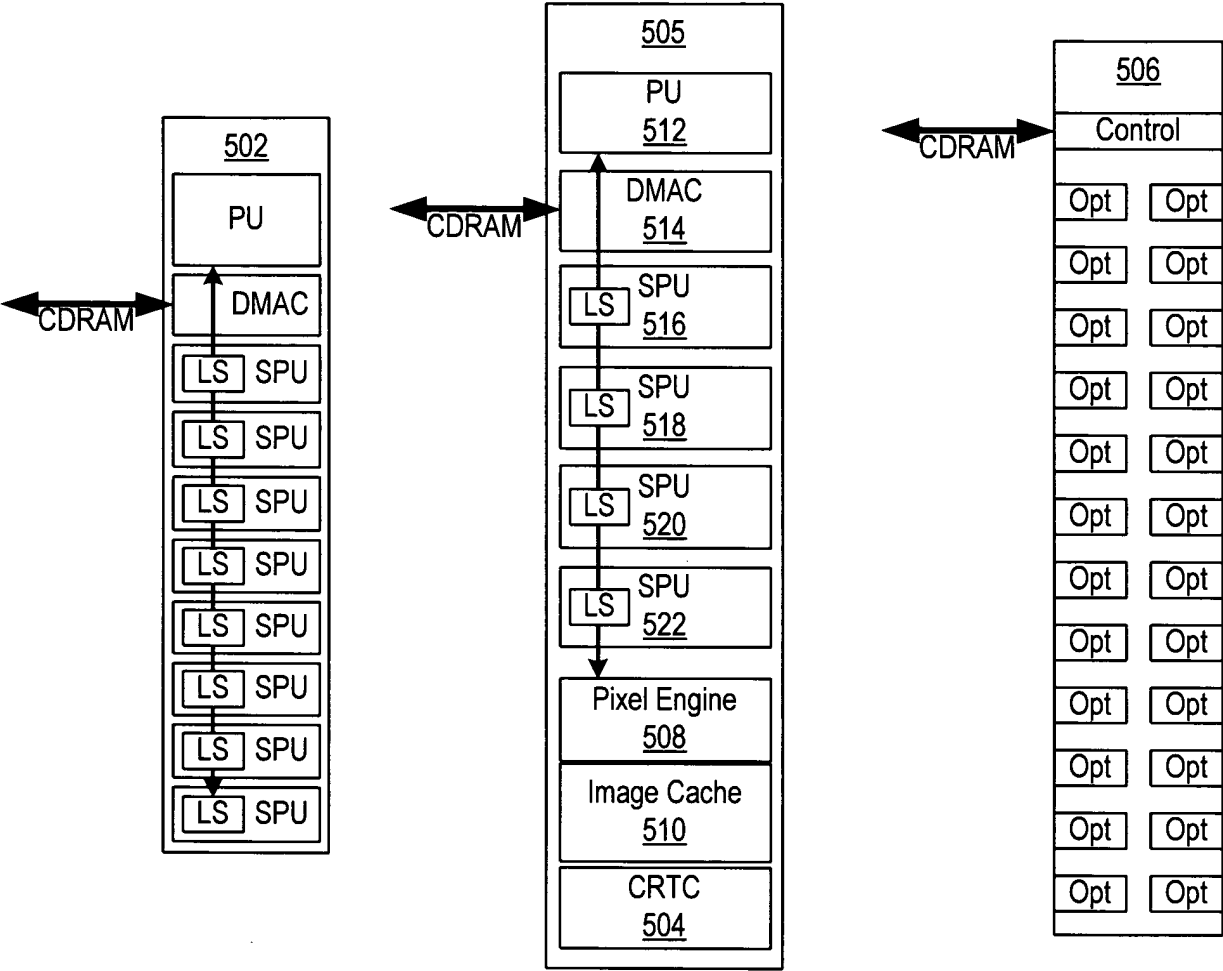
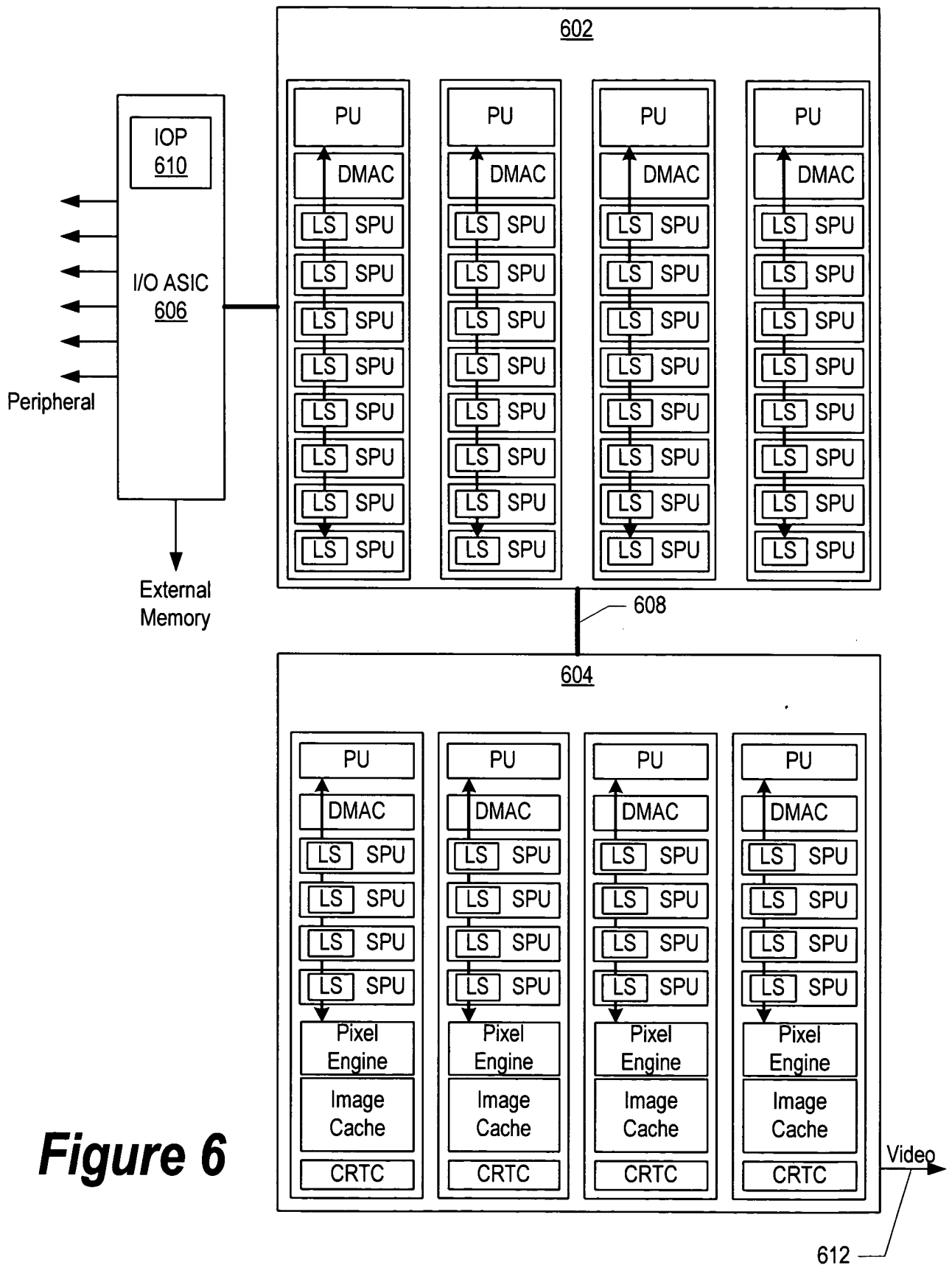


Figure 5

6/49



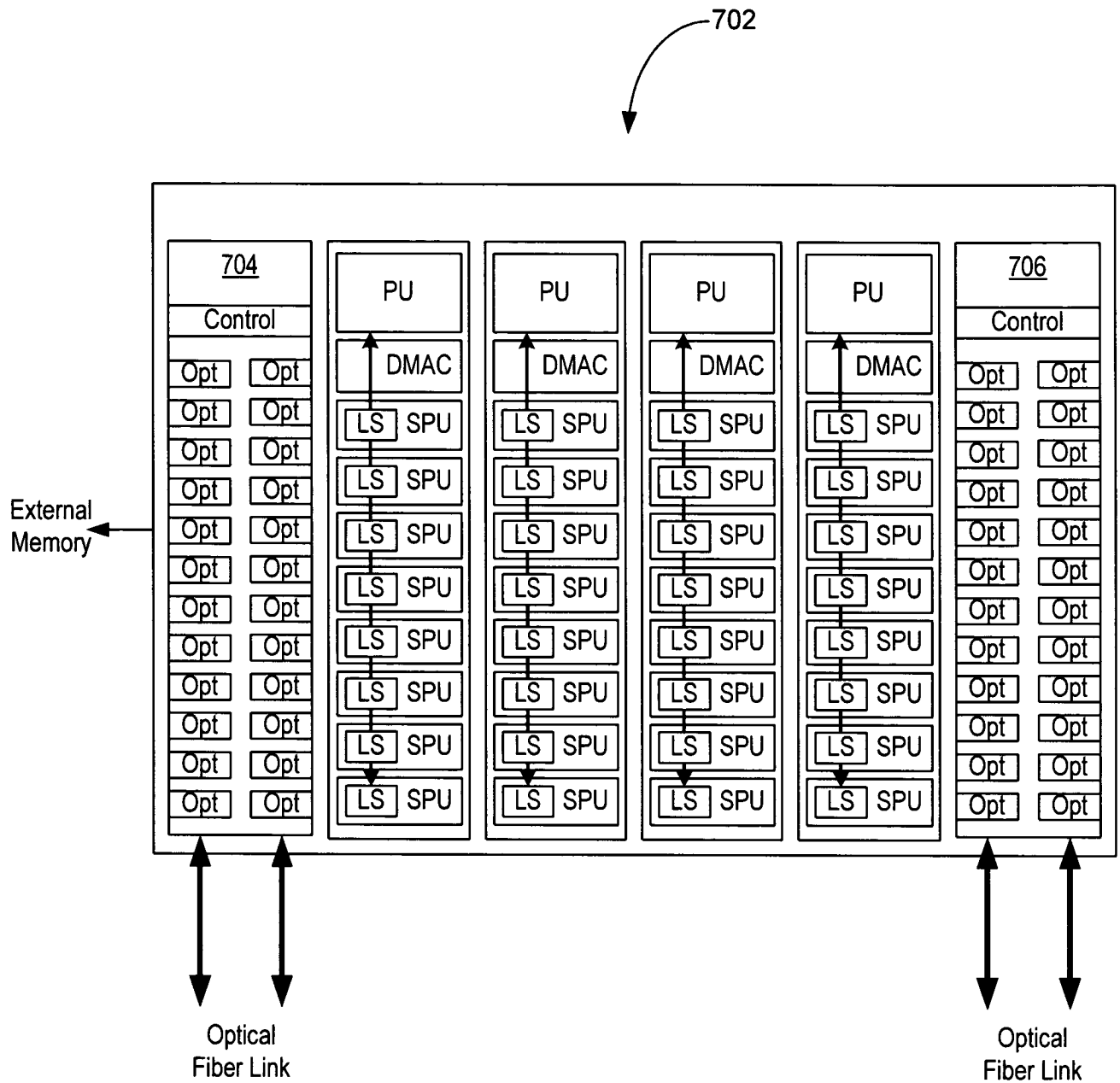
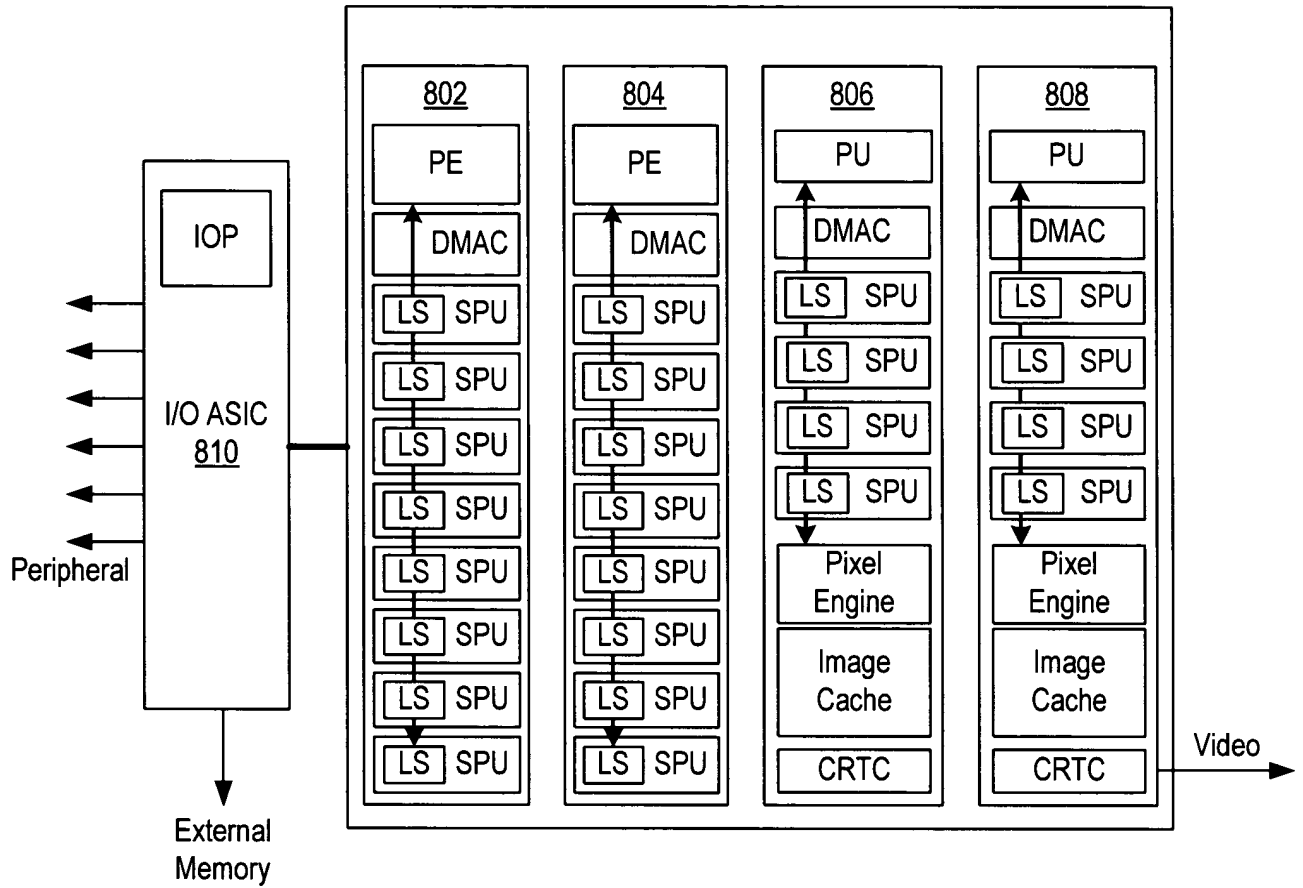


Figure 7

**Figure 8**

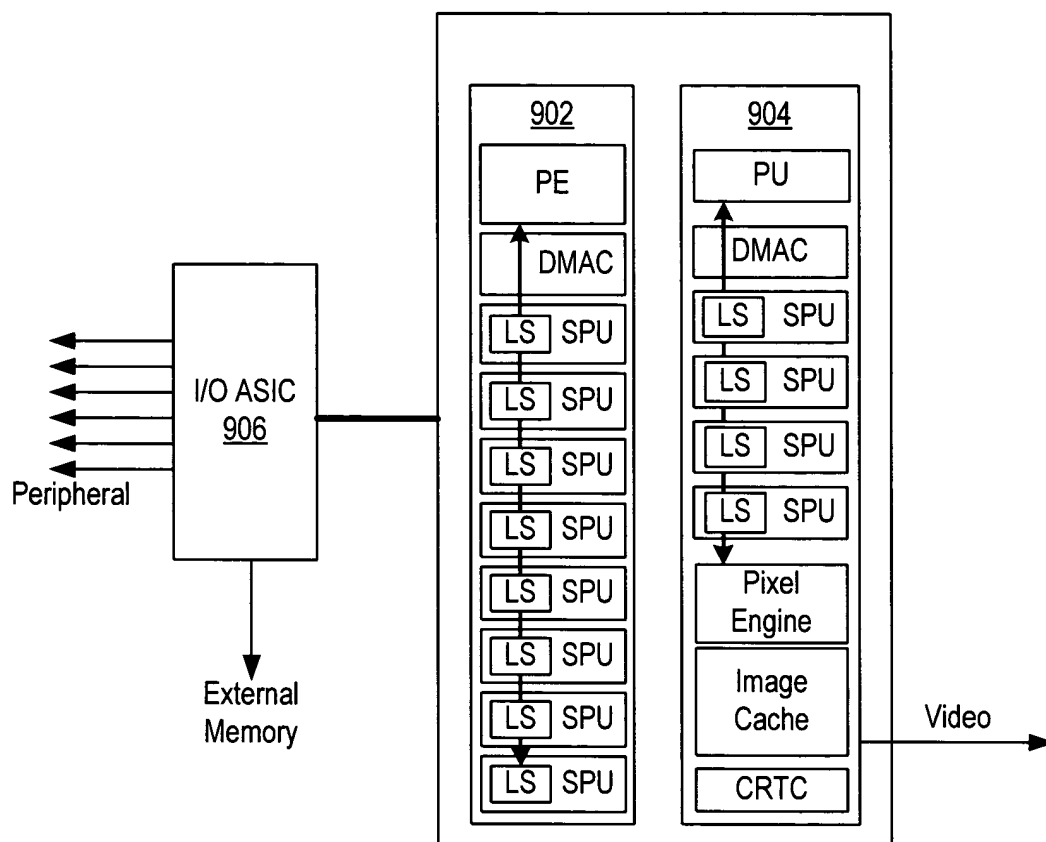


Figure 9

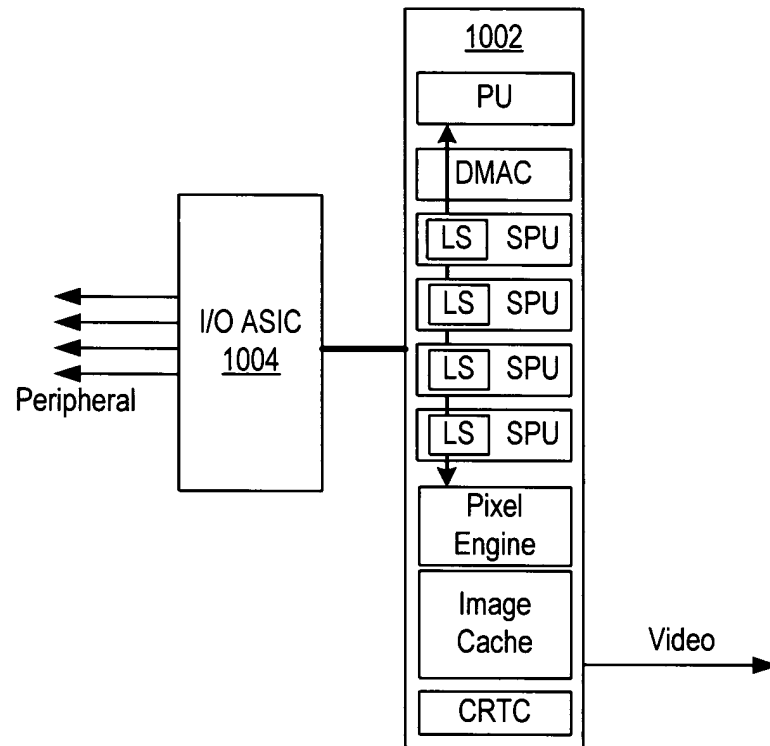


Figure 10

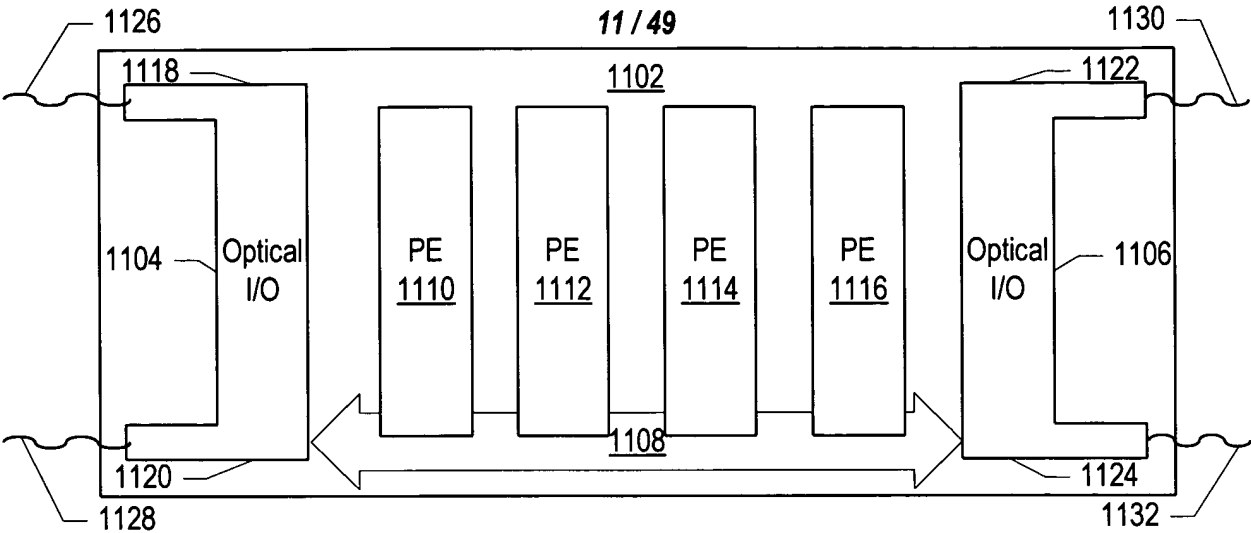


Figure 11A

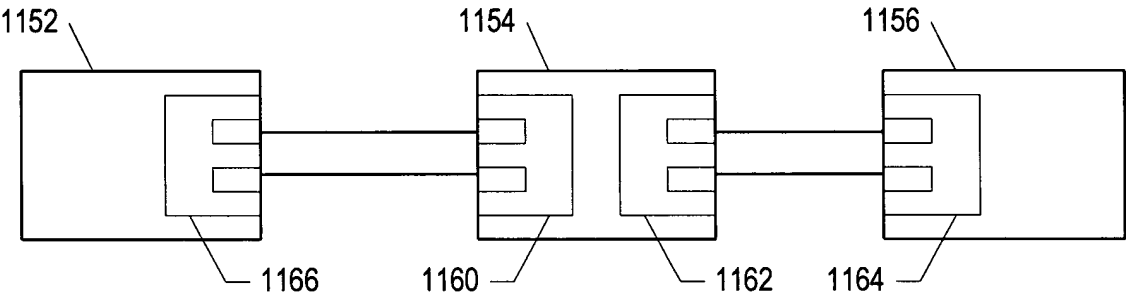


Figure 11B

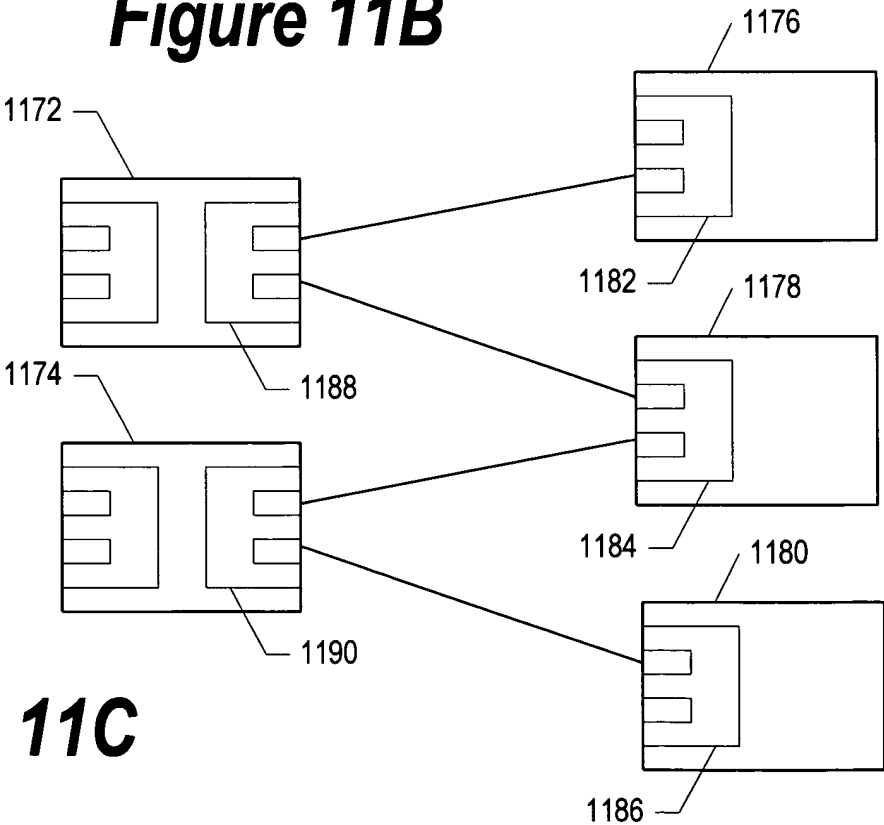
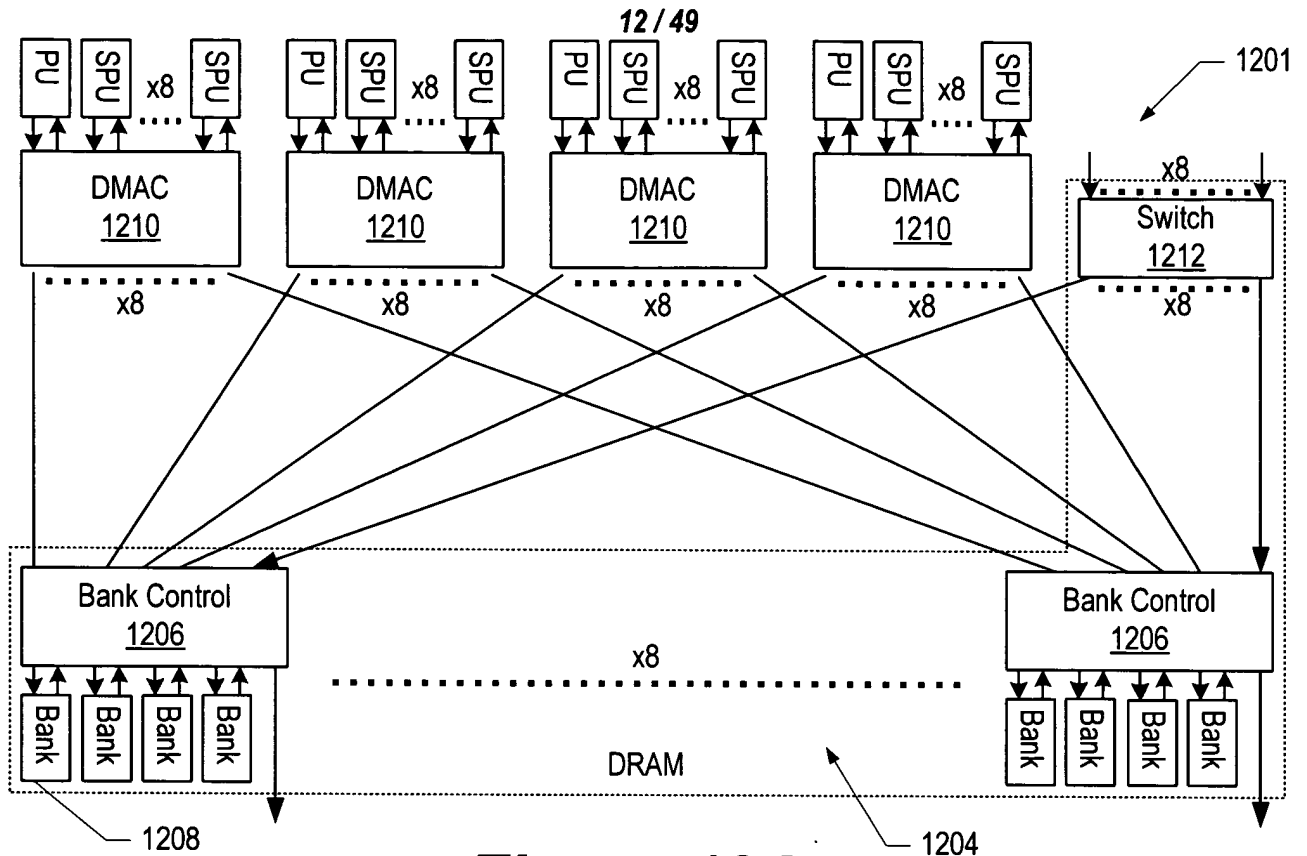
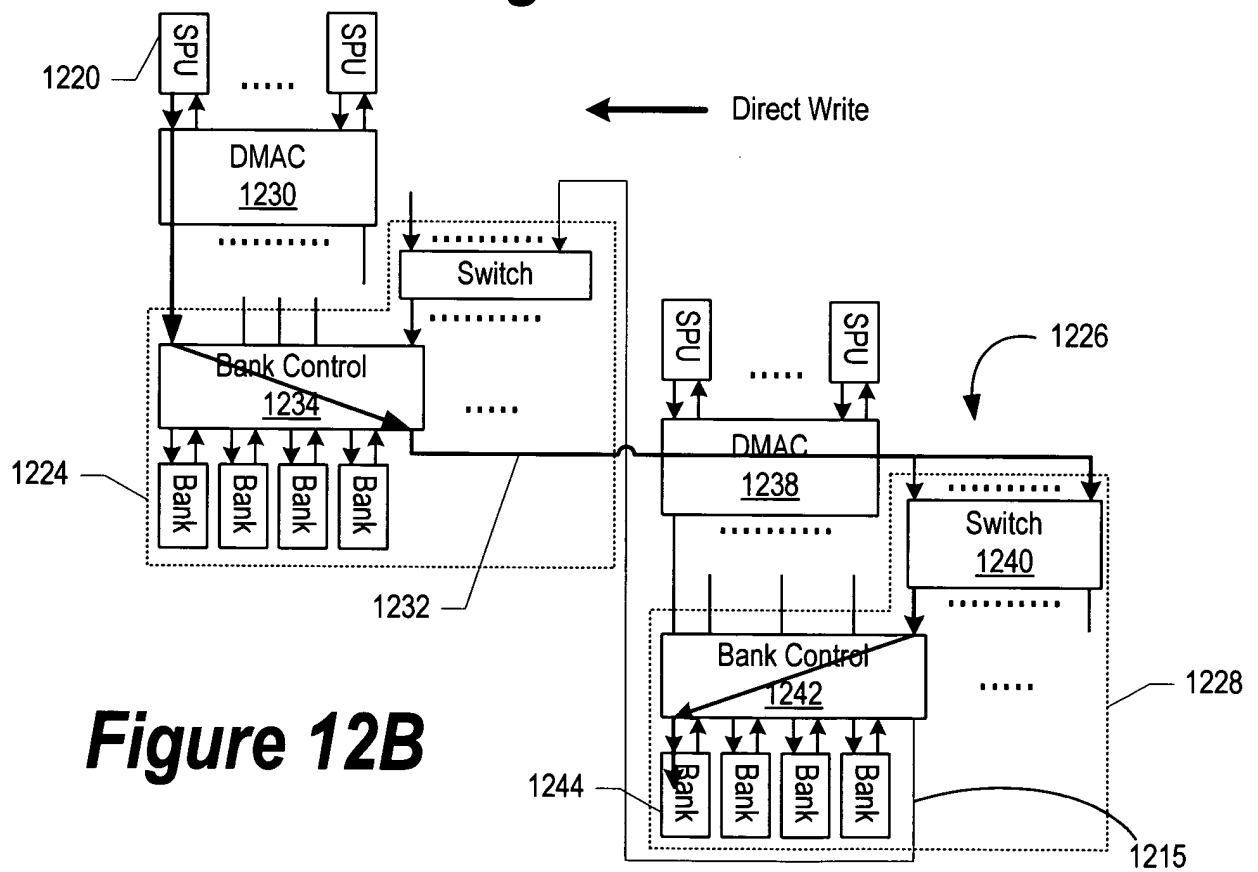
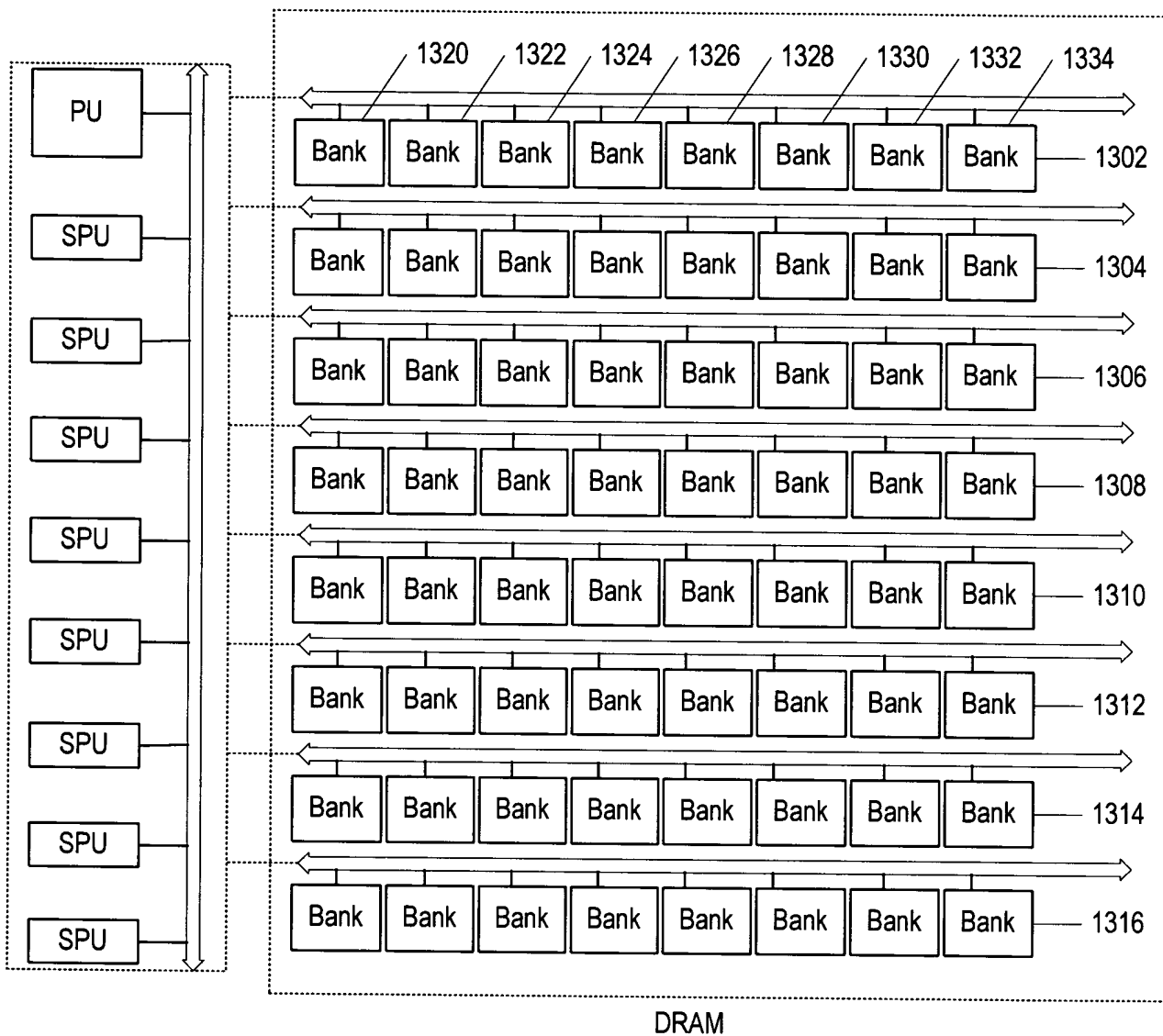


Figure 11C

**Figure 12A****Figure 12B**

**Figure 13**

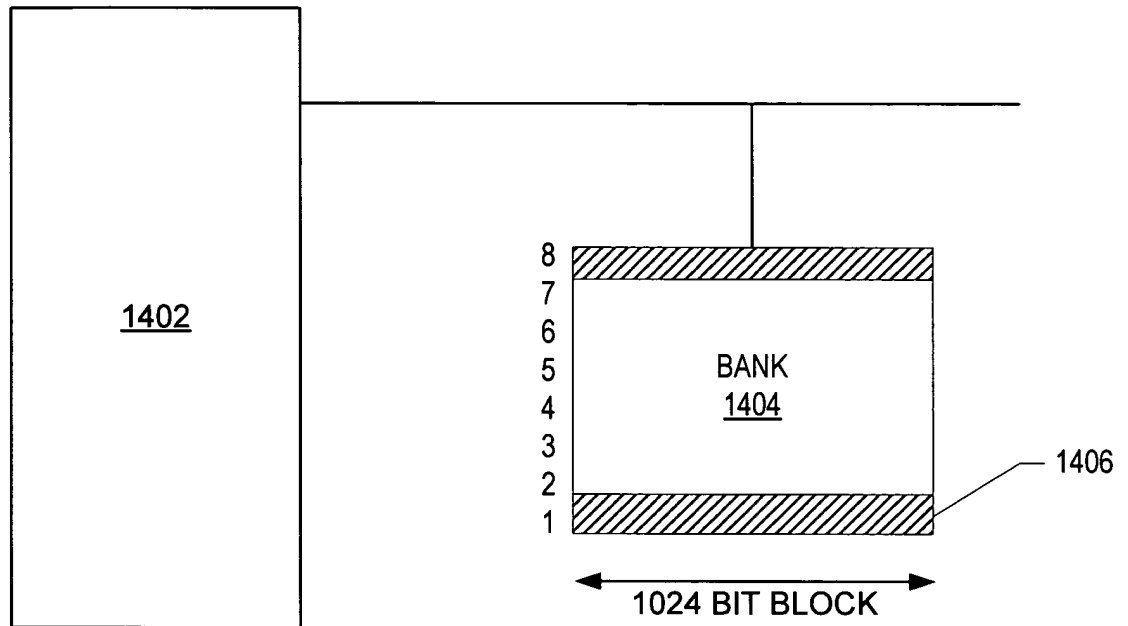


Figure 14A

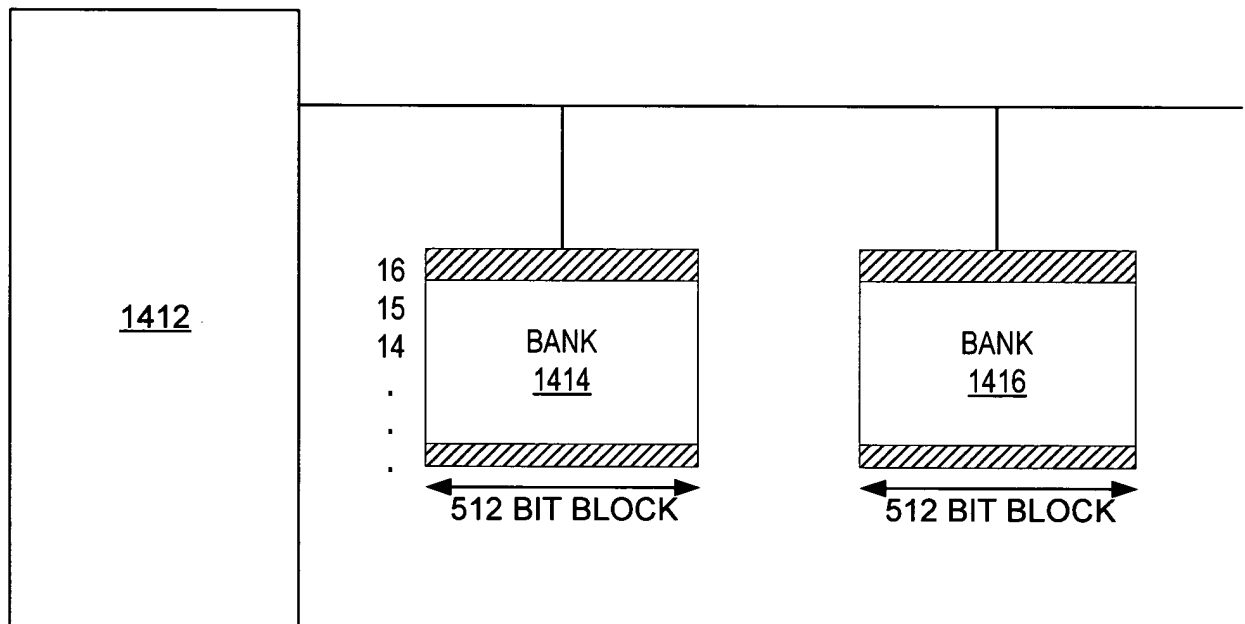
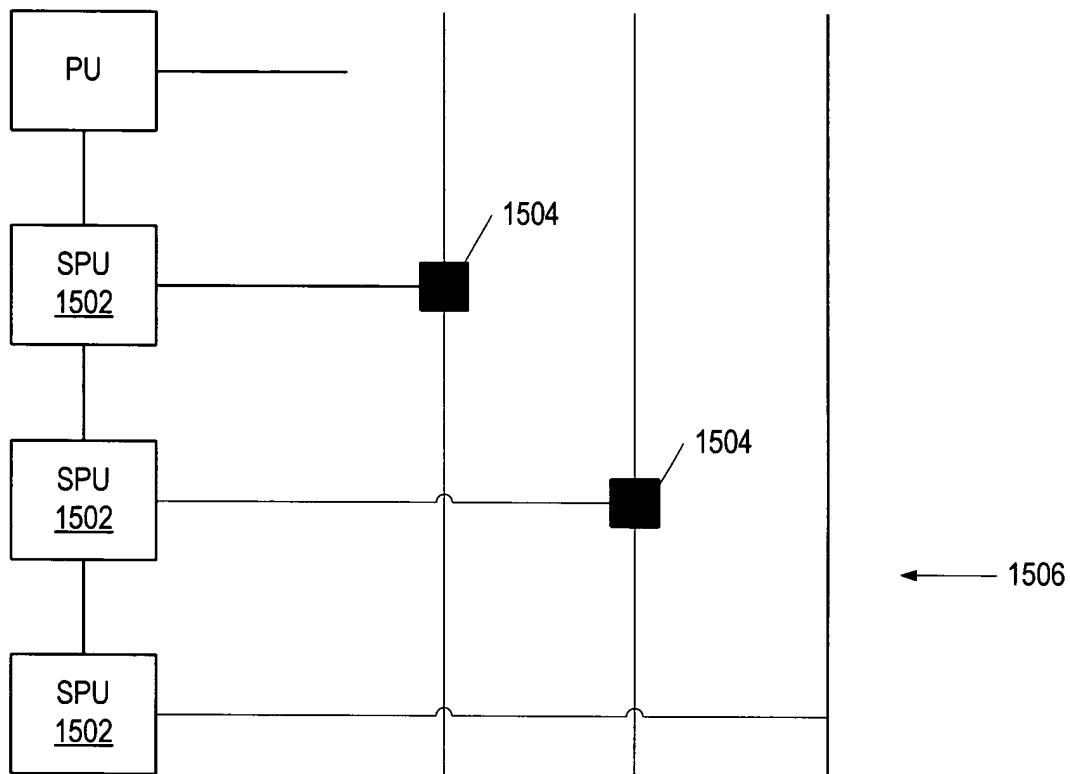
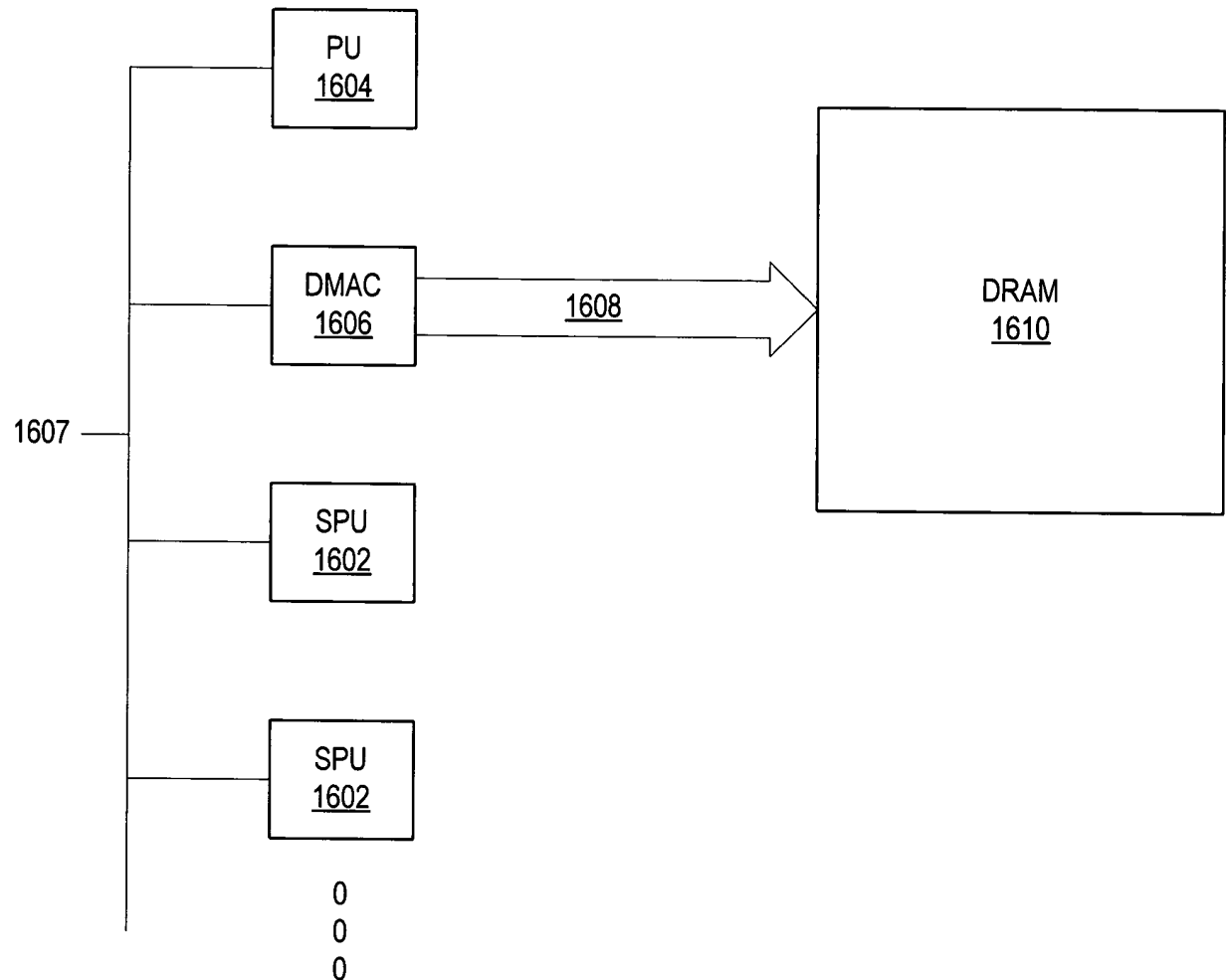


Figure 14B

**Figure 15**

**Figure 16**

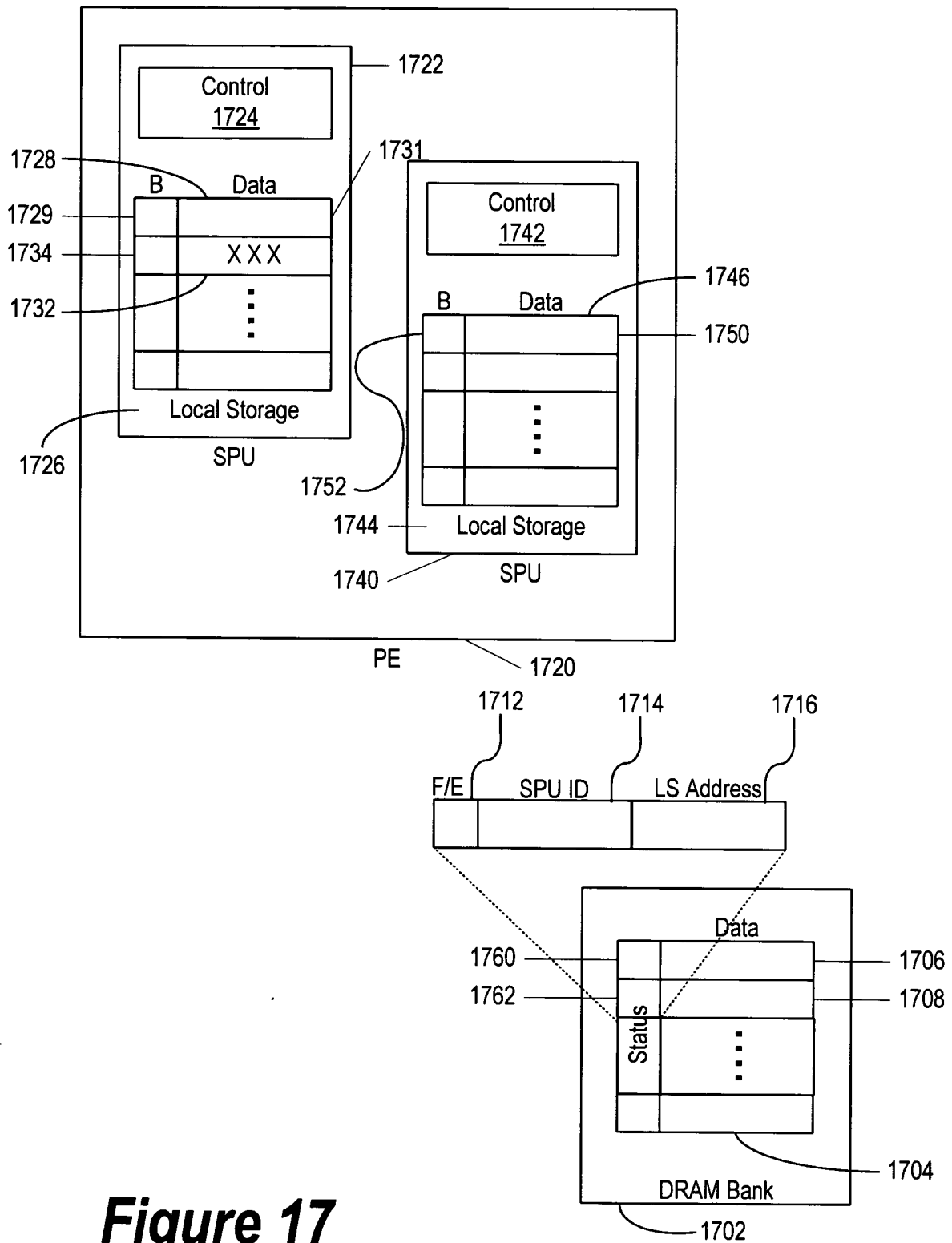


Figure 17

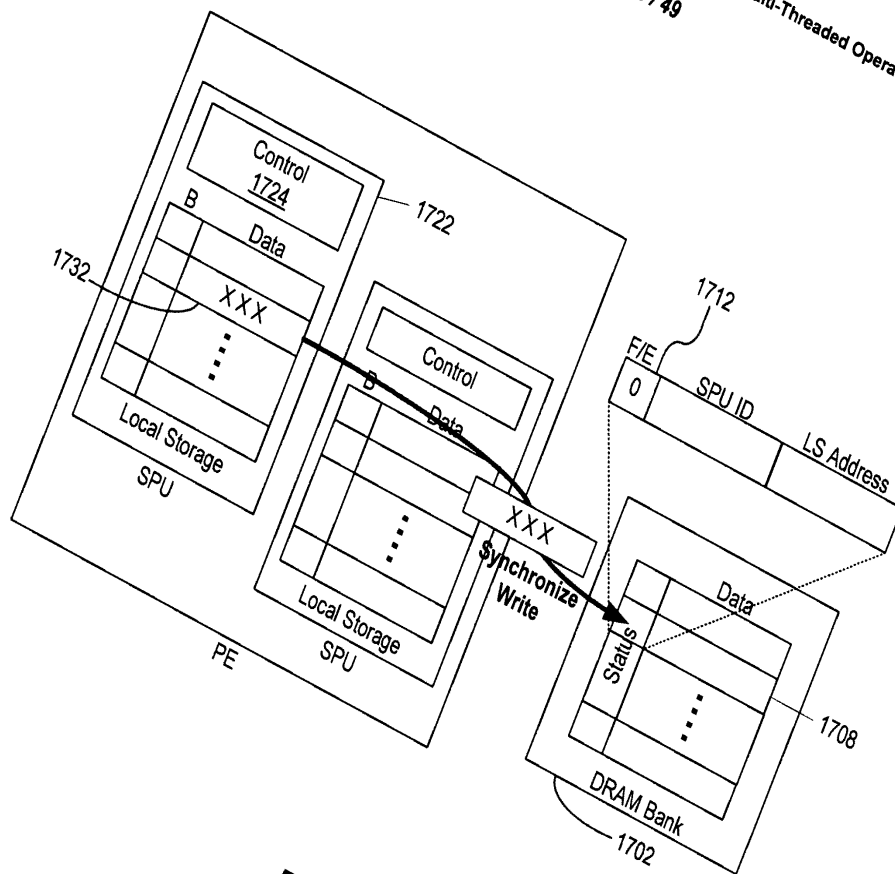


Figure 18

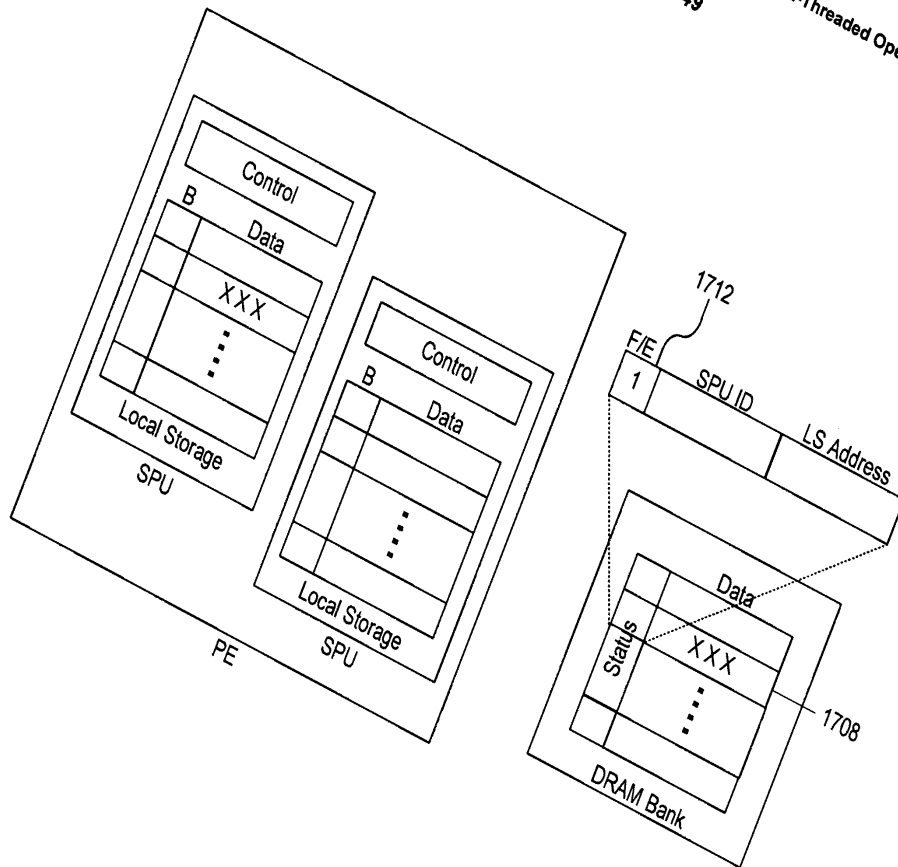


Figure 19

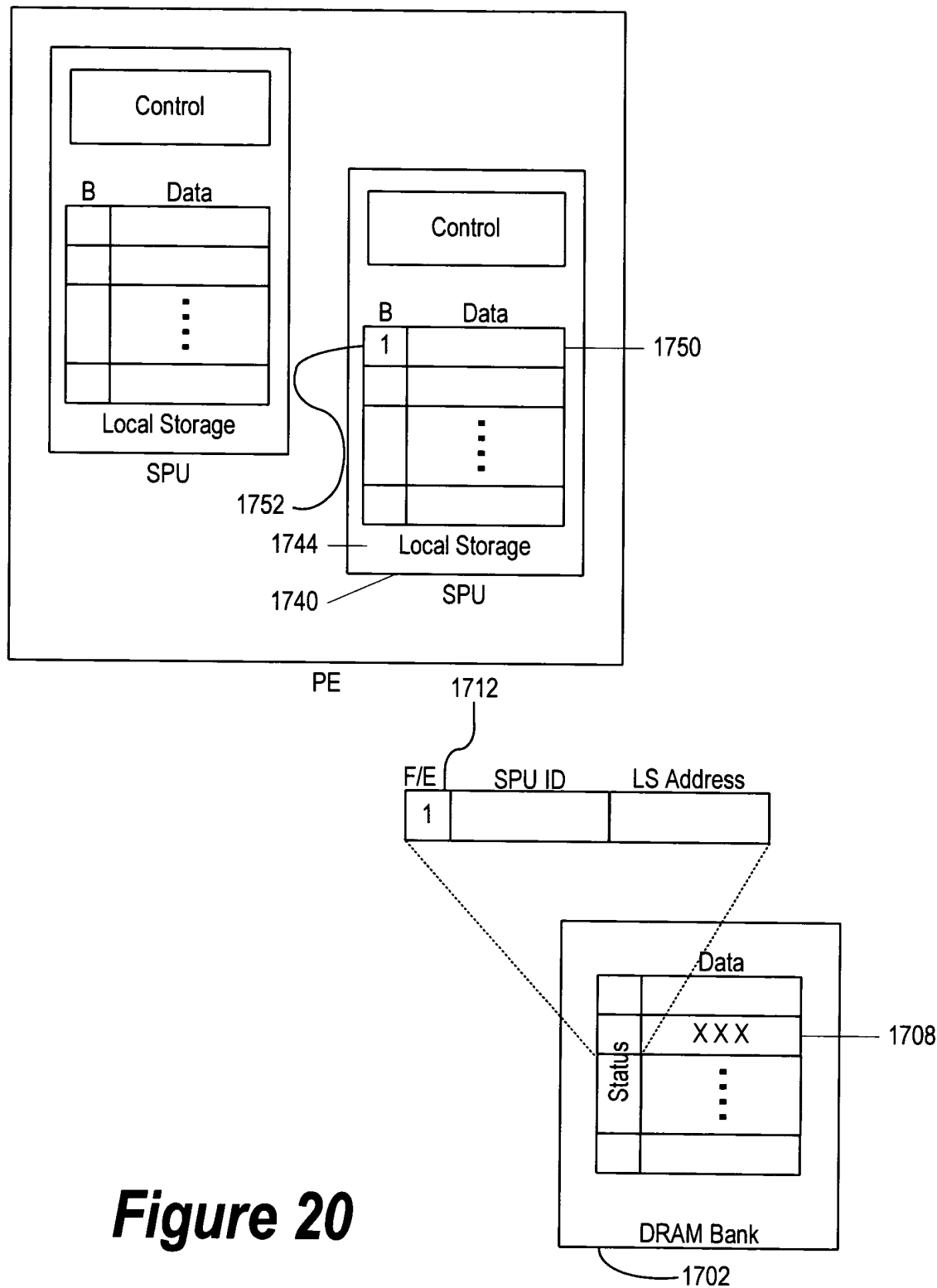


Figure 20

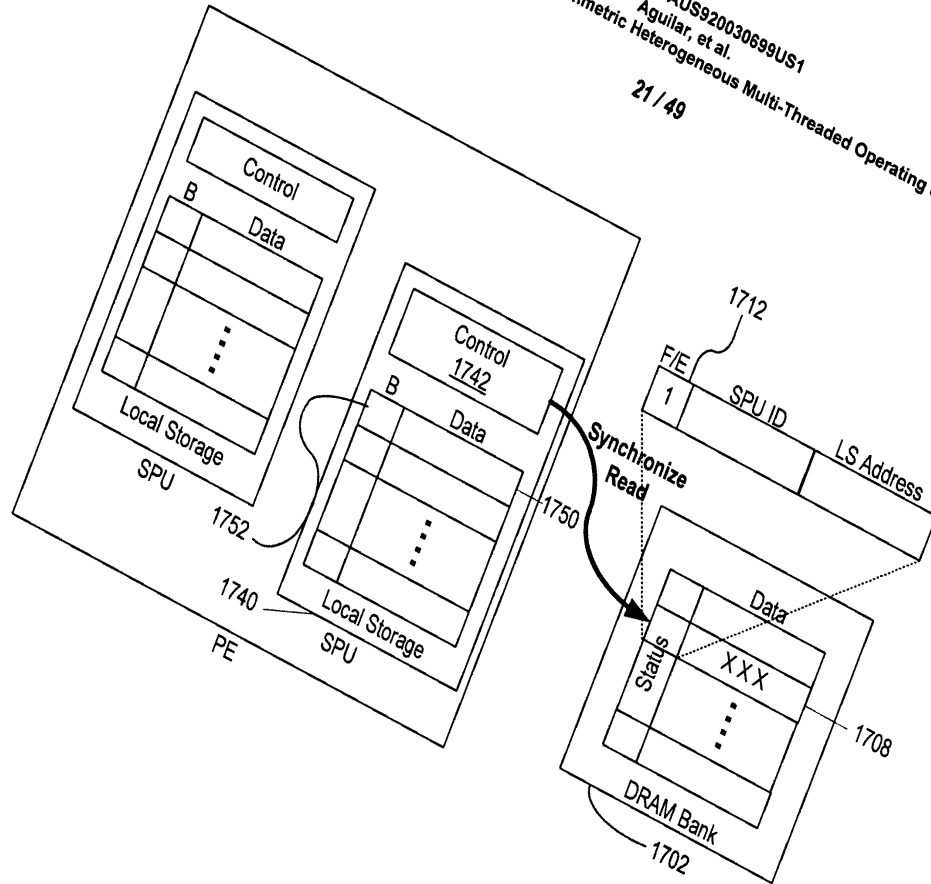


Figure 21

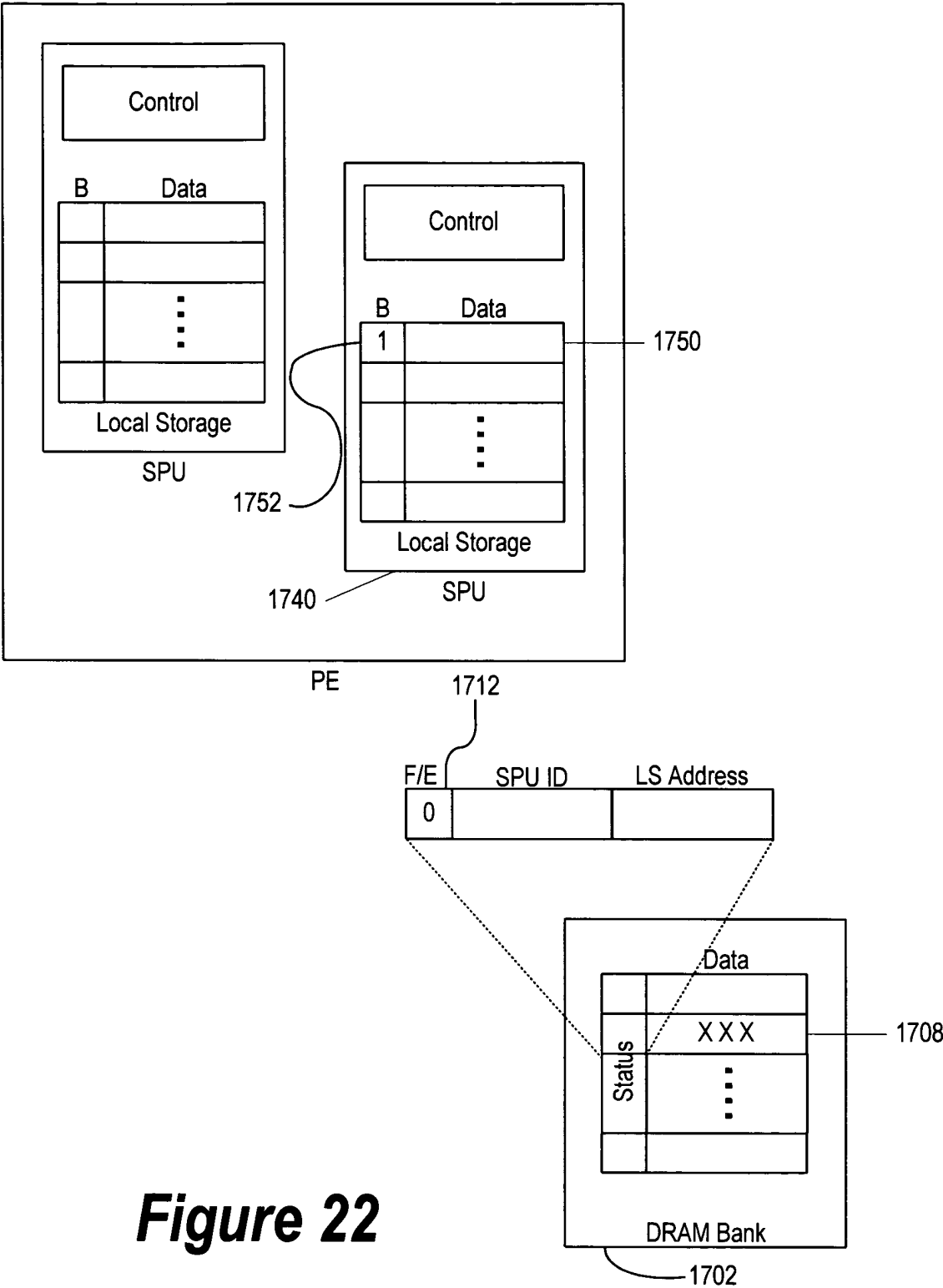
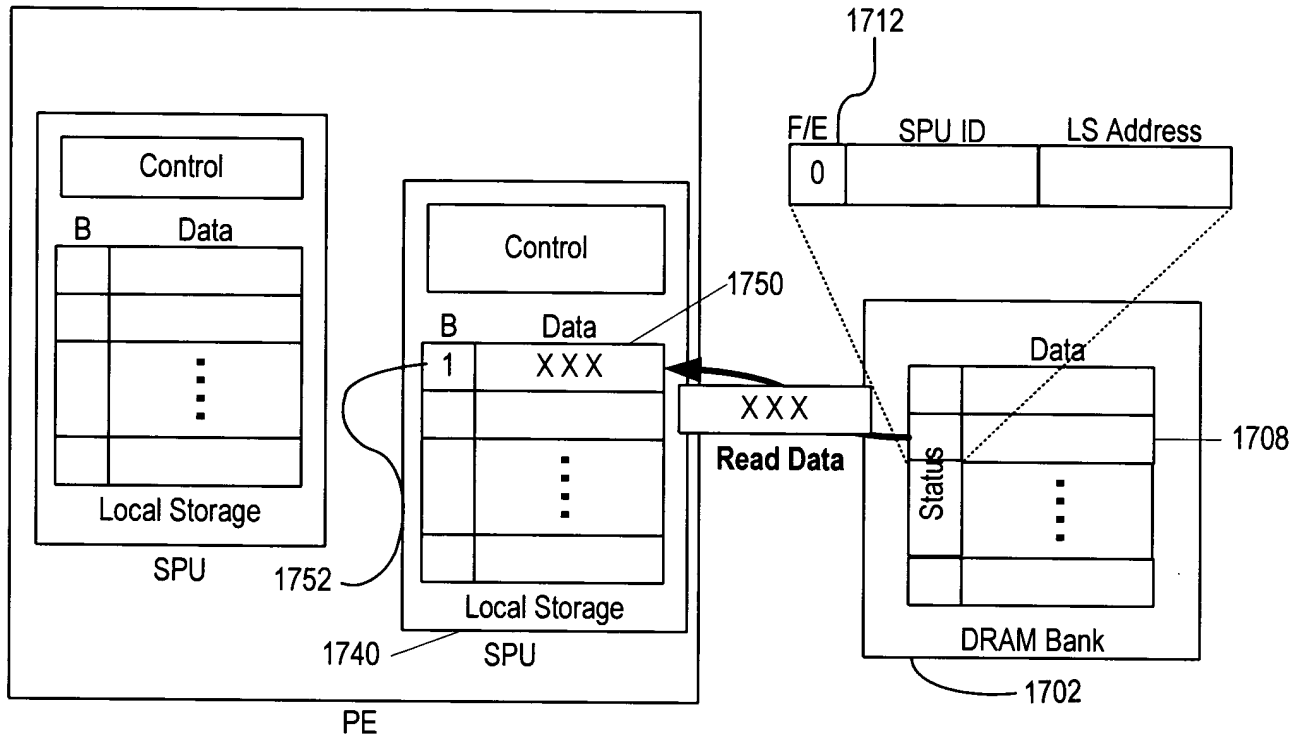
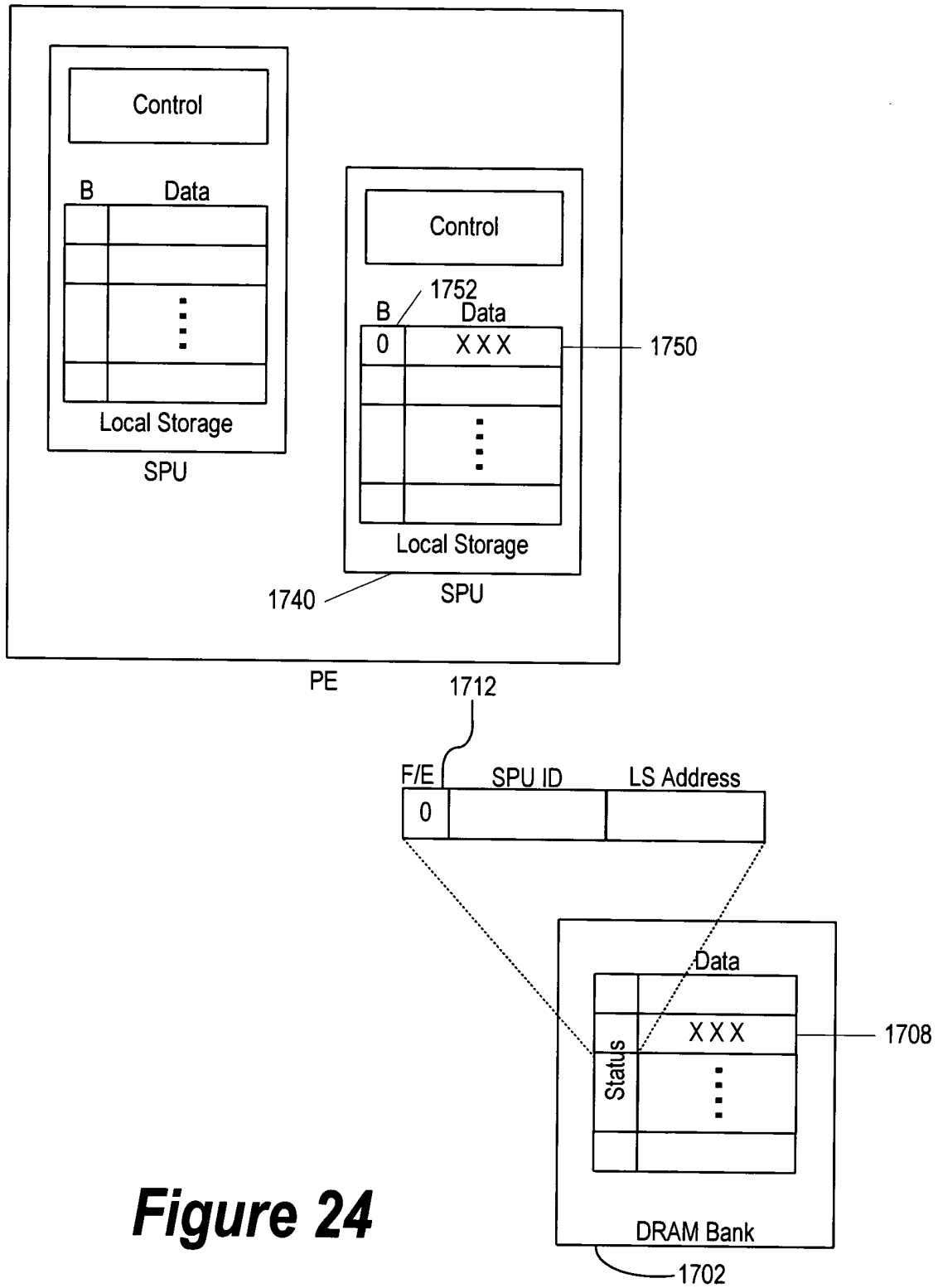
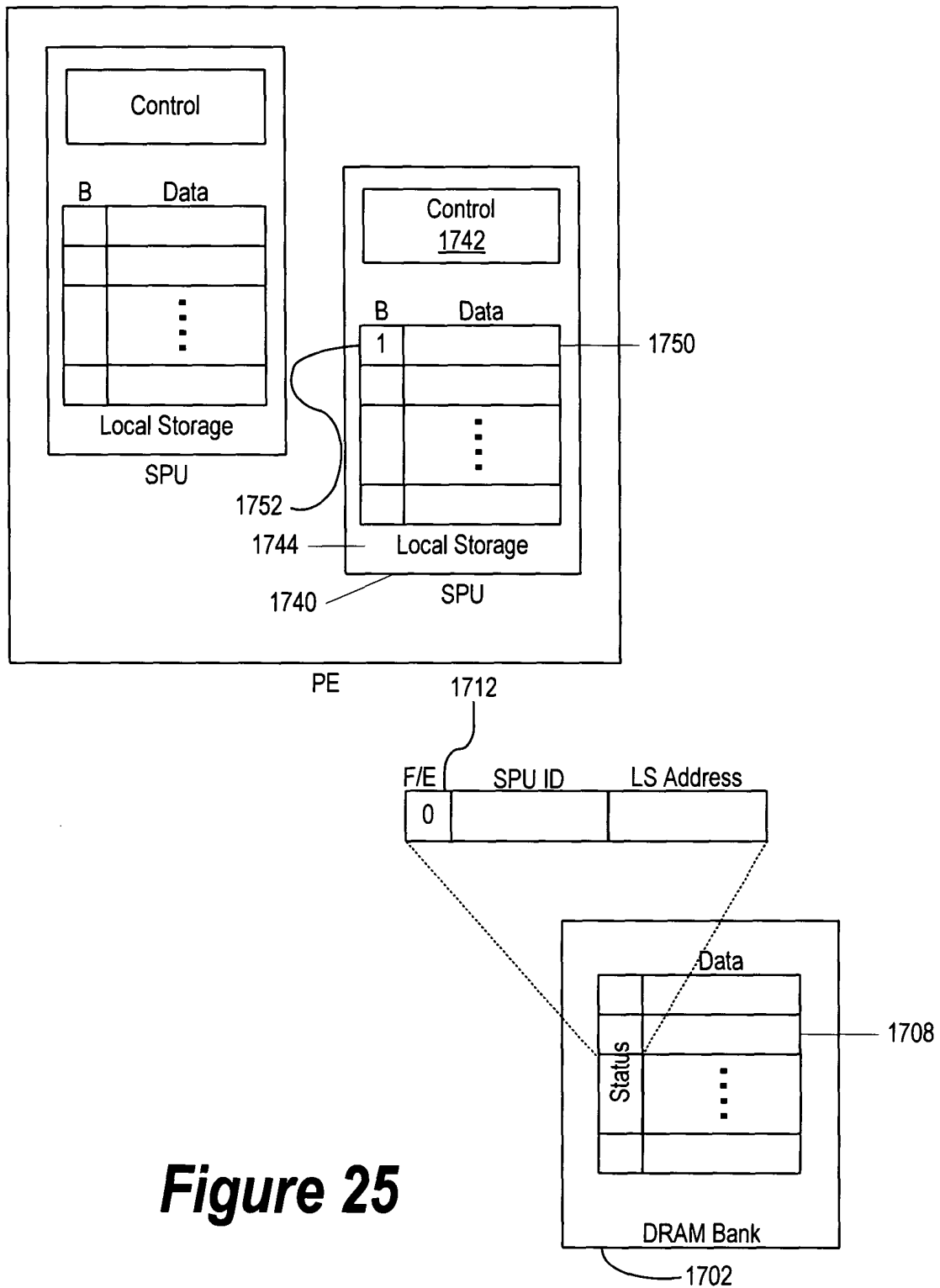


Figure 22

**Figure 23**

**Figure 24**

**Figure 25**

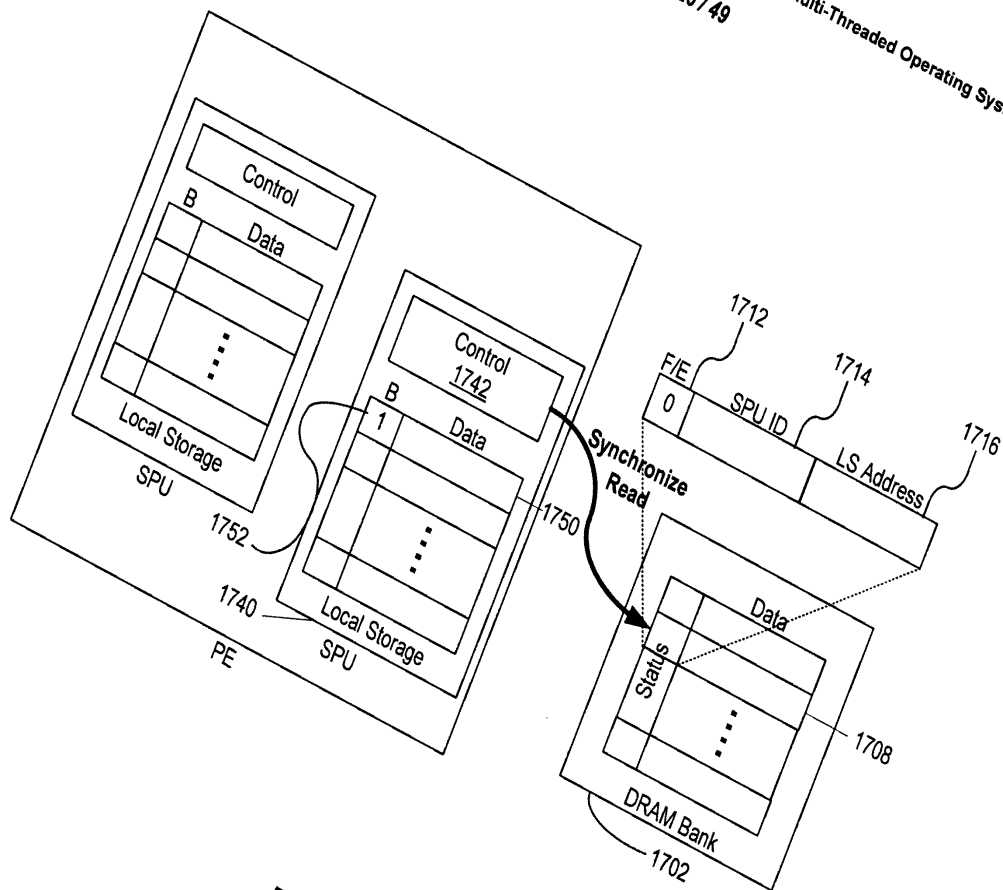


Figure 26

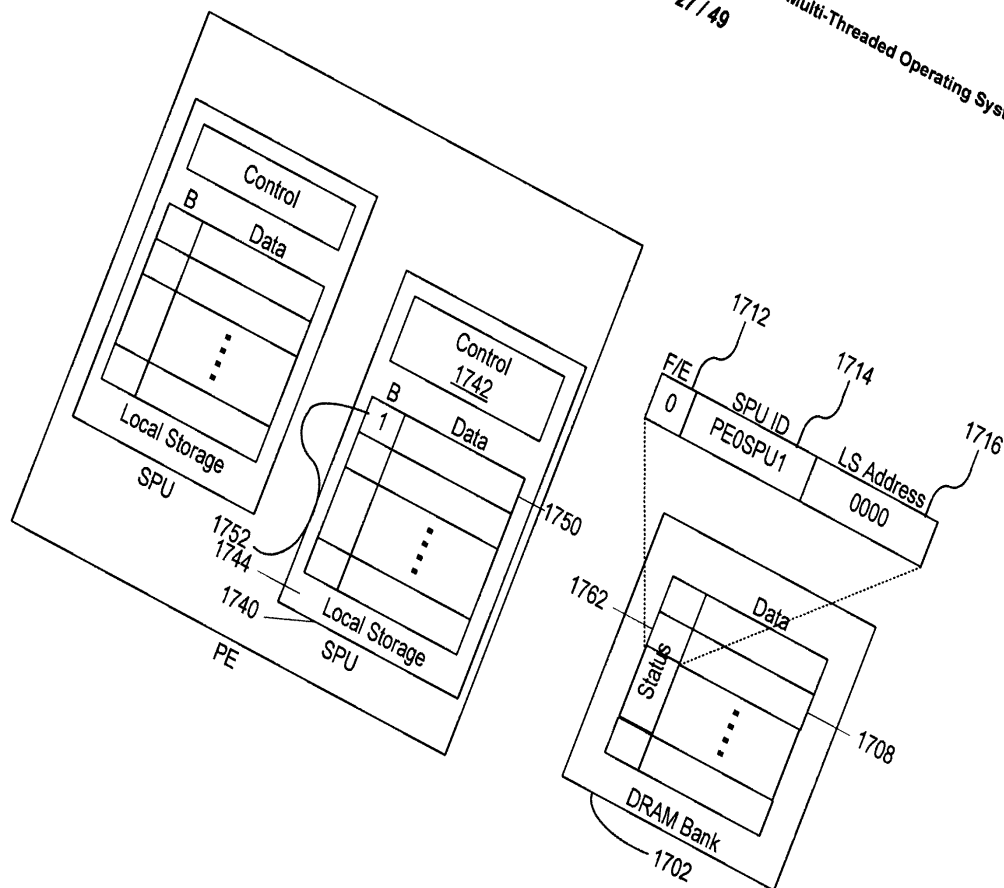


Figure 27

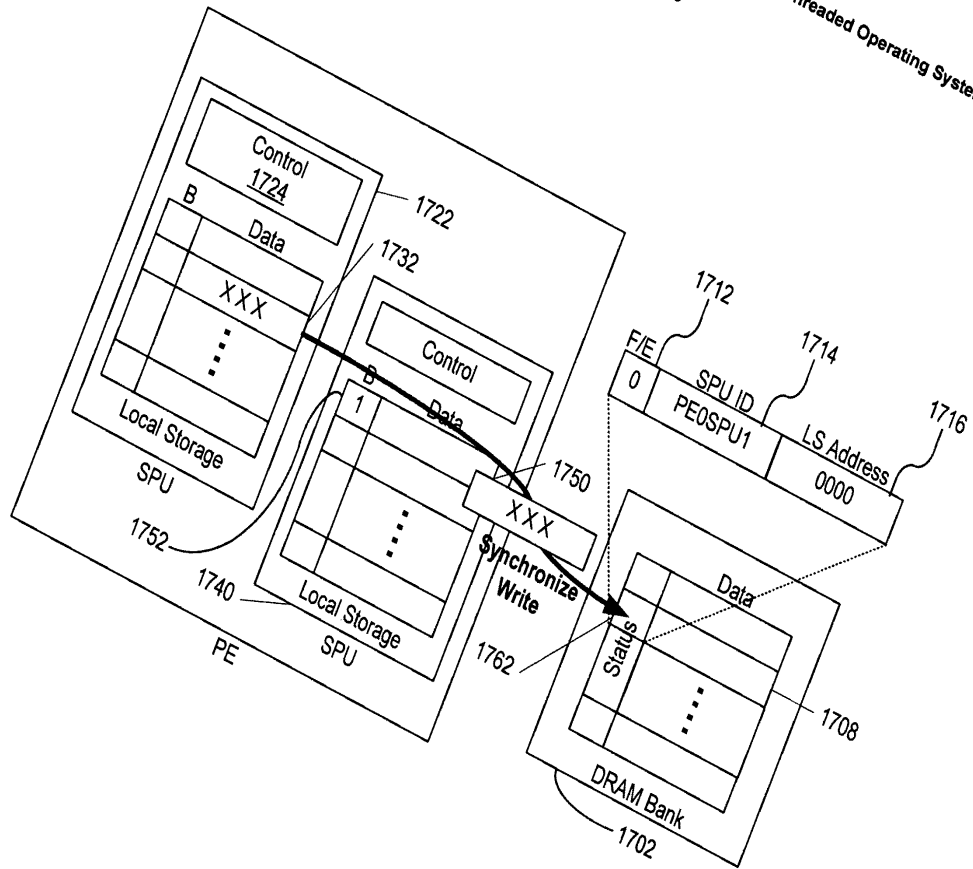


Figure 28

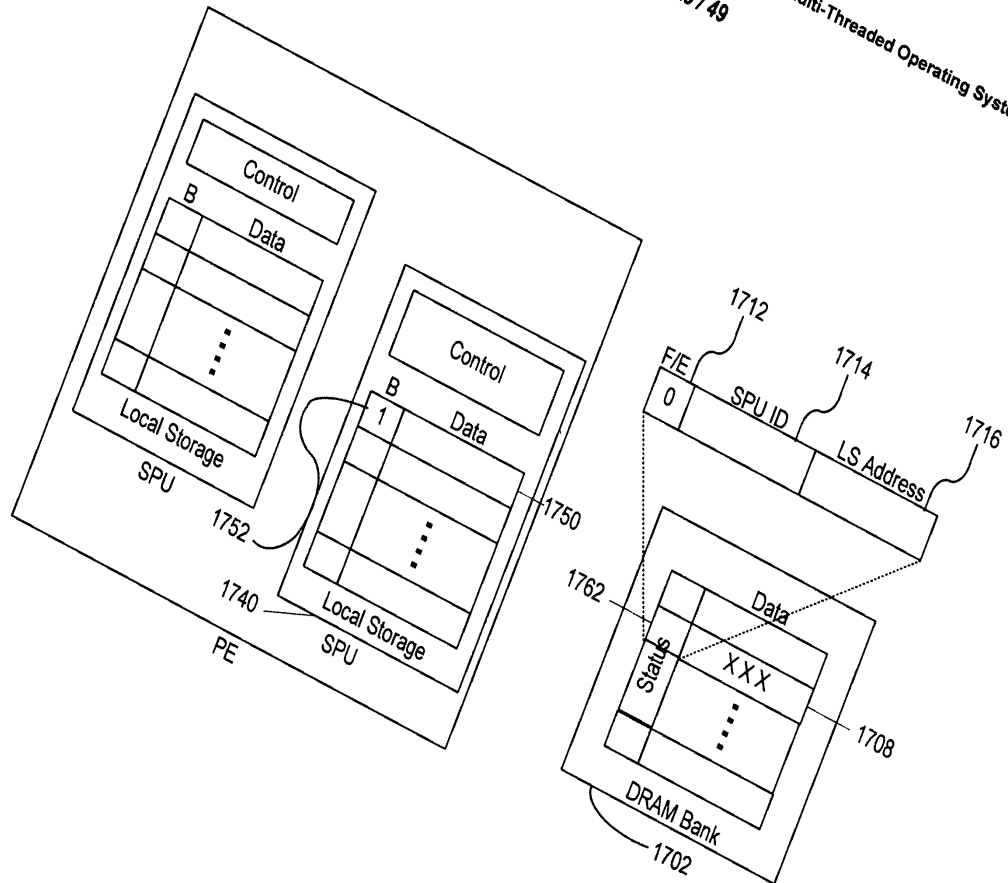


Figure 29

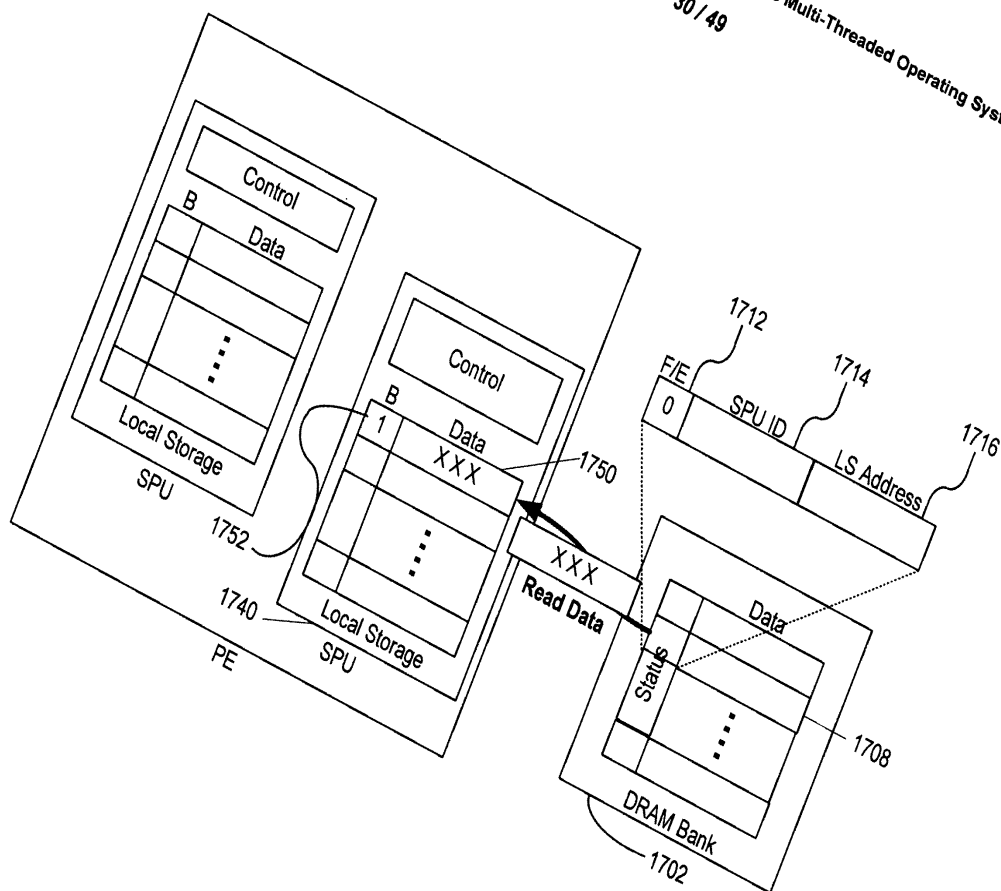
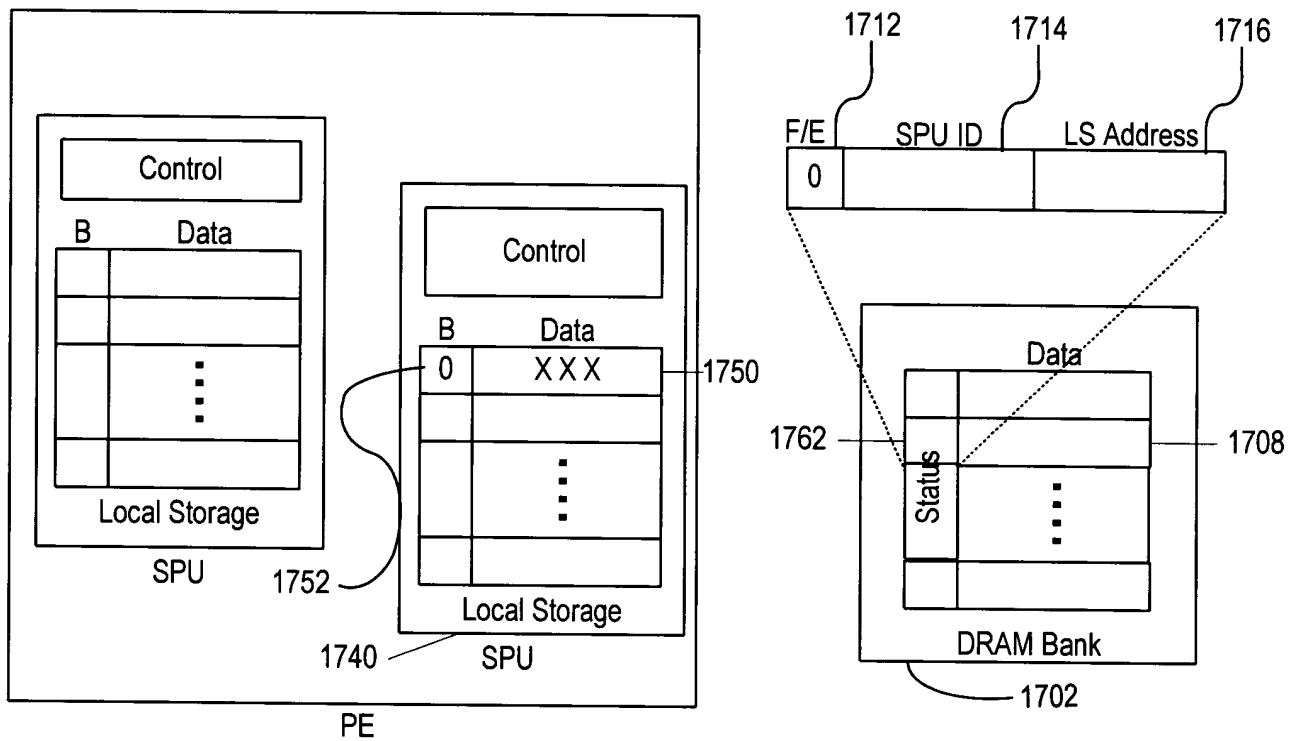
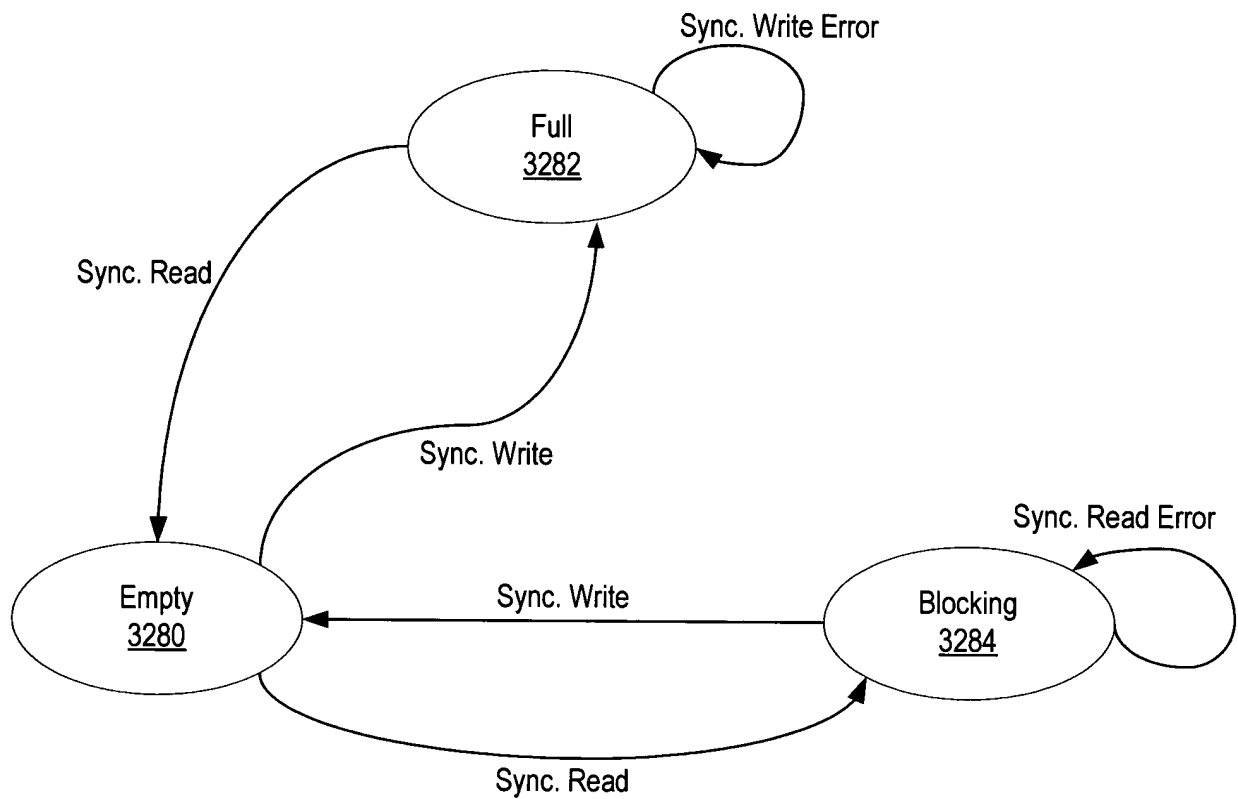


Figure 30

**Figure 31**

**Figure 32**

Key Control Table

3302

3304 ID 3306 3308

0	SPU Key	Key Mask
1	SPU Key	Key Mask
2	SPU Key	Key Mask
		⋮
7	SPU Key	Key Mask

Figure 33

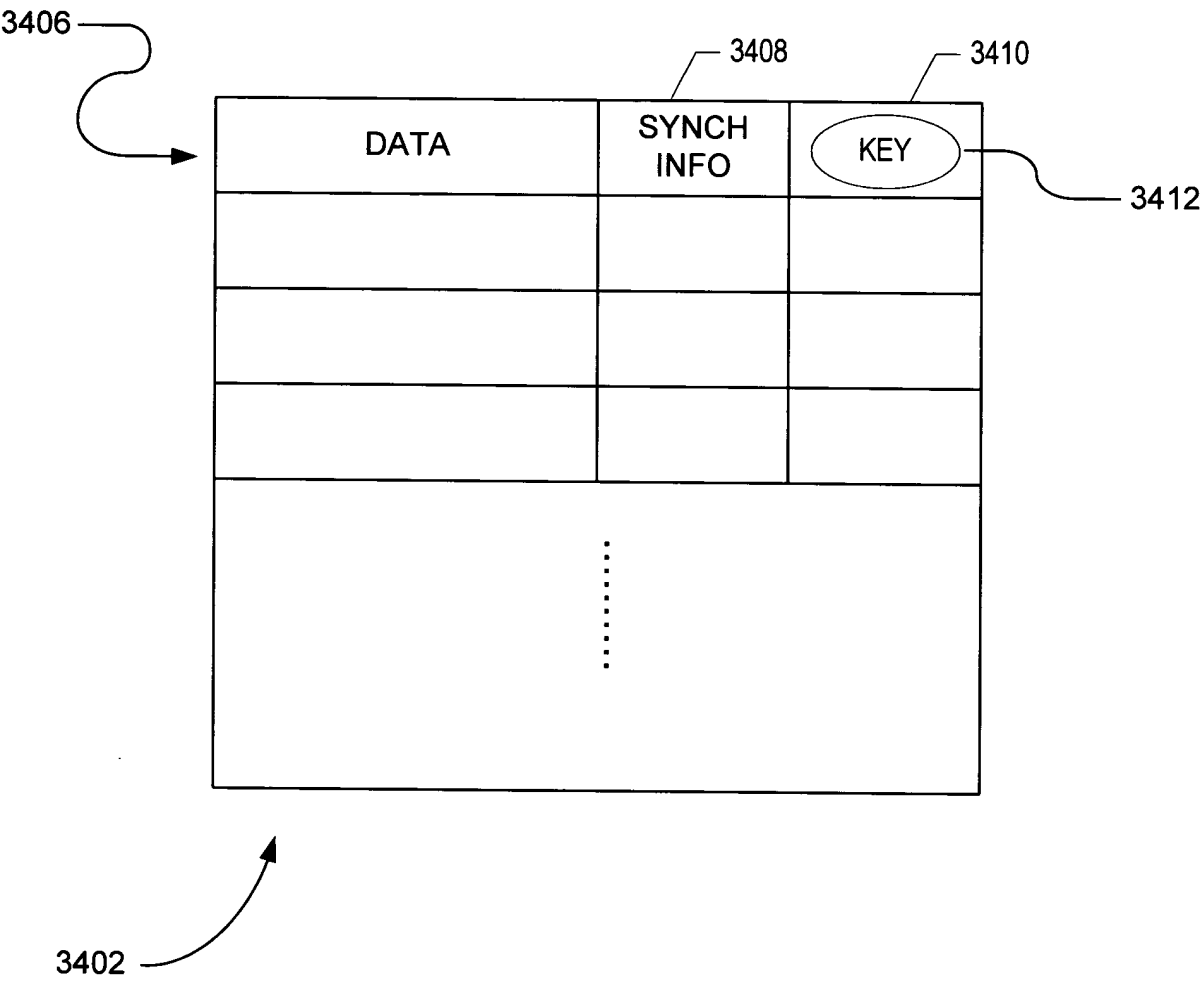


Figure 34

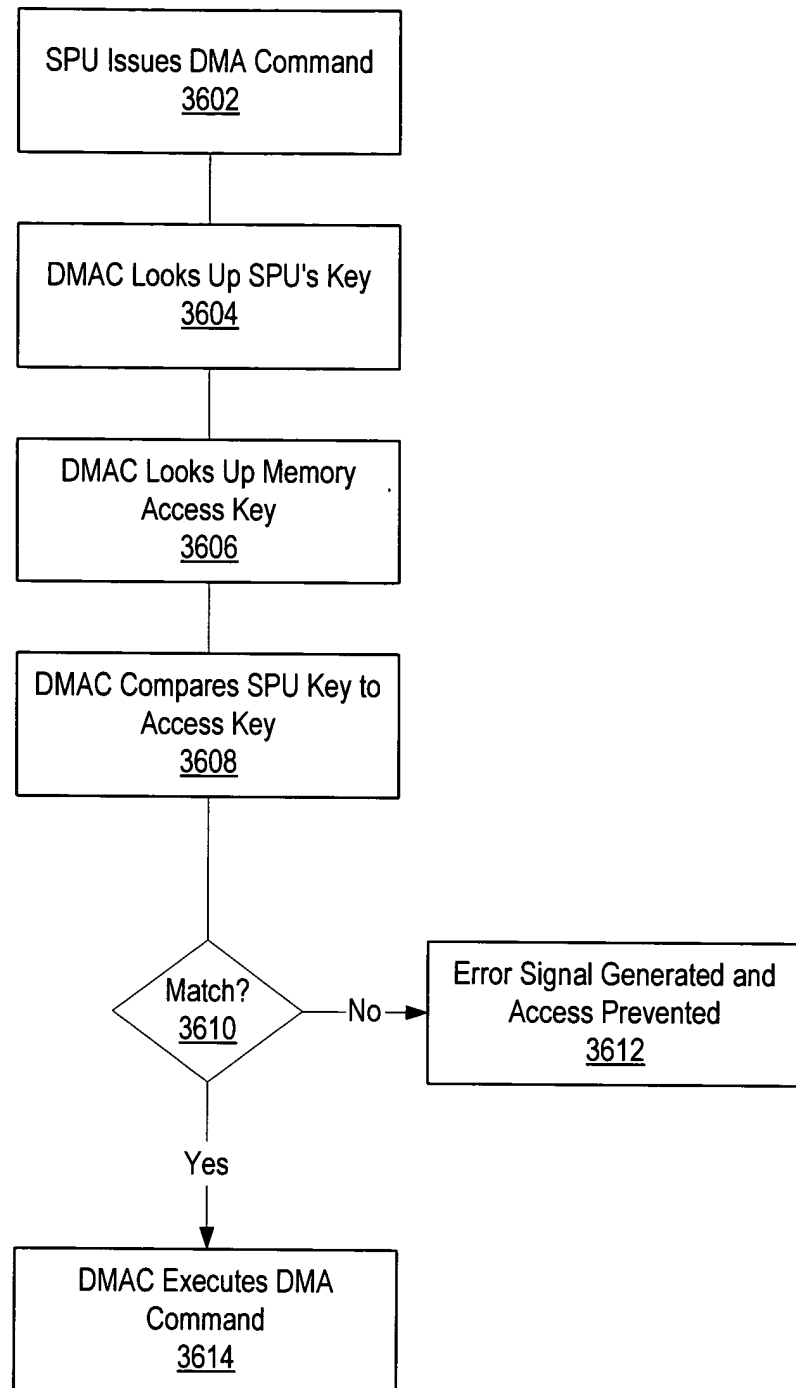
Memory Access Control Table

3504	ID	3506		3508	3510	3512
		0	Base	Size	Access Key	Access Key Mask
		1	Base	Size	Access Key	Access Key Mask
		2	Base	Size	Access Key	Access Key Mask
		⋮				
	63	Base	Size	Access Key	Access Key Mask	

3502

Figure 35

36 / 49

**Figure 36**

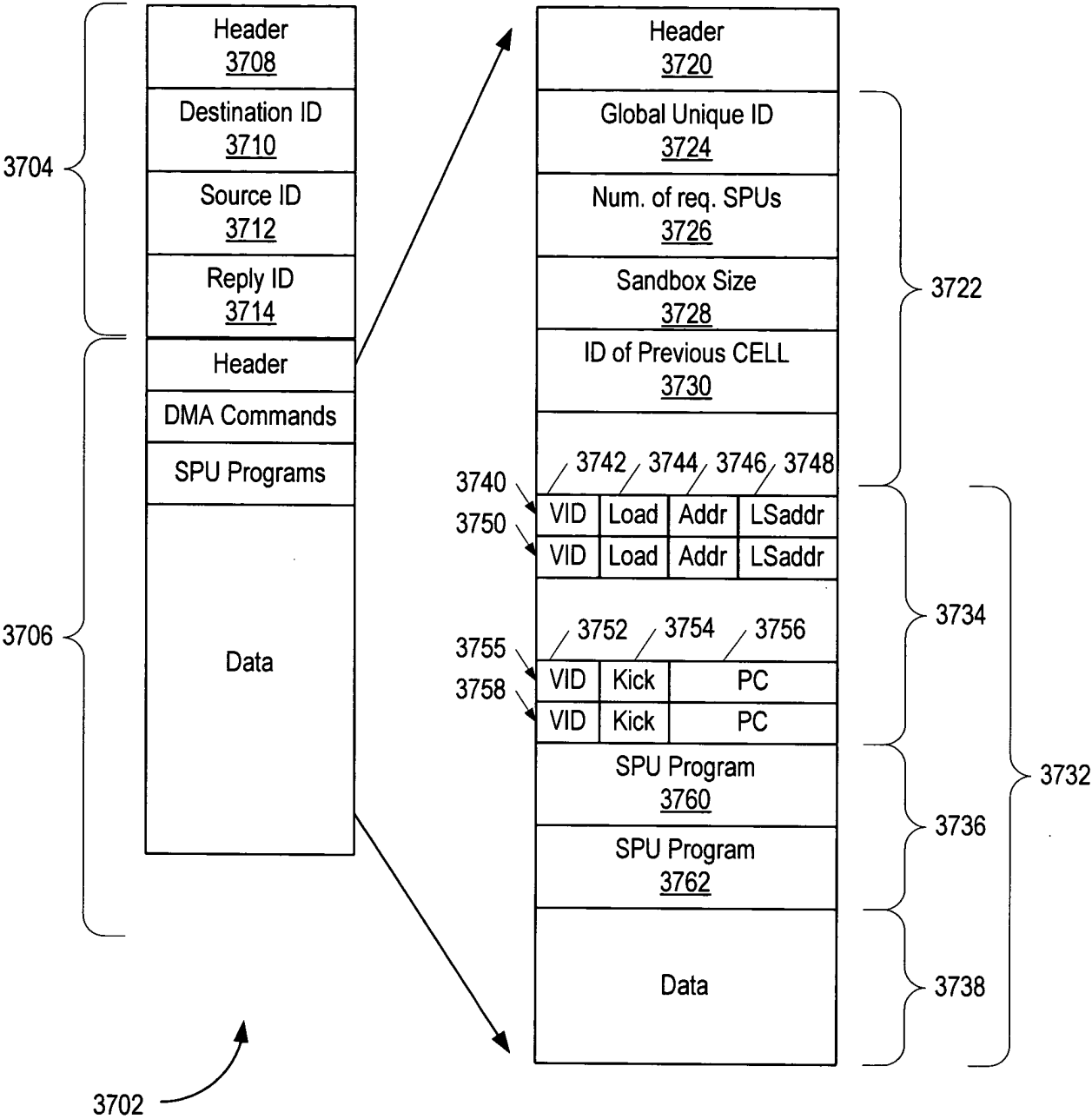
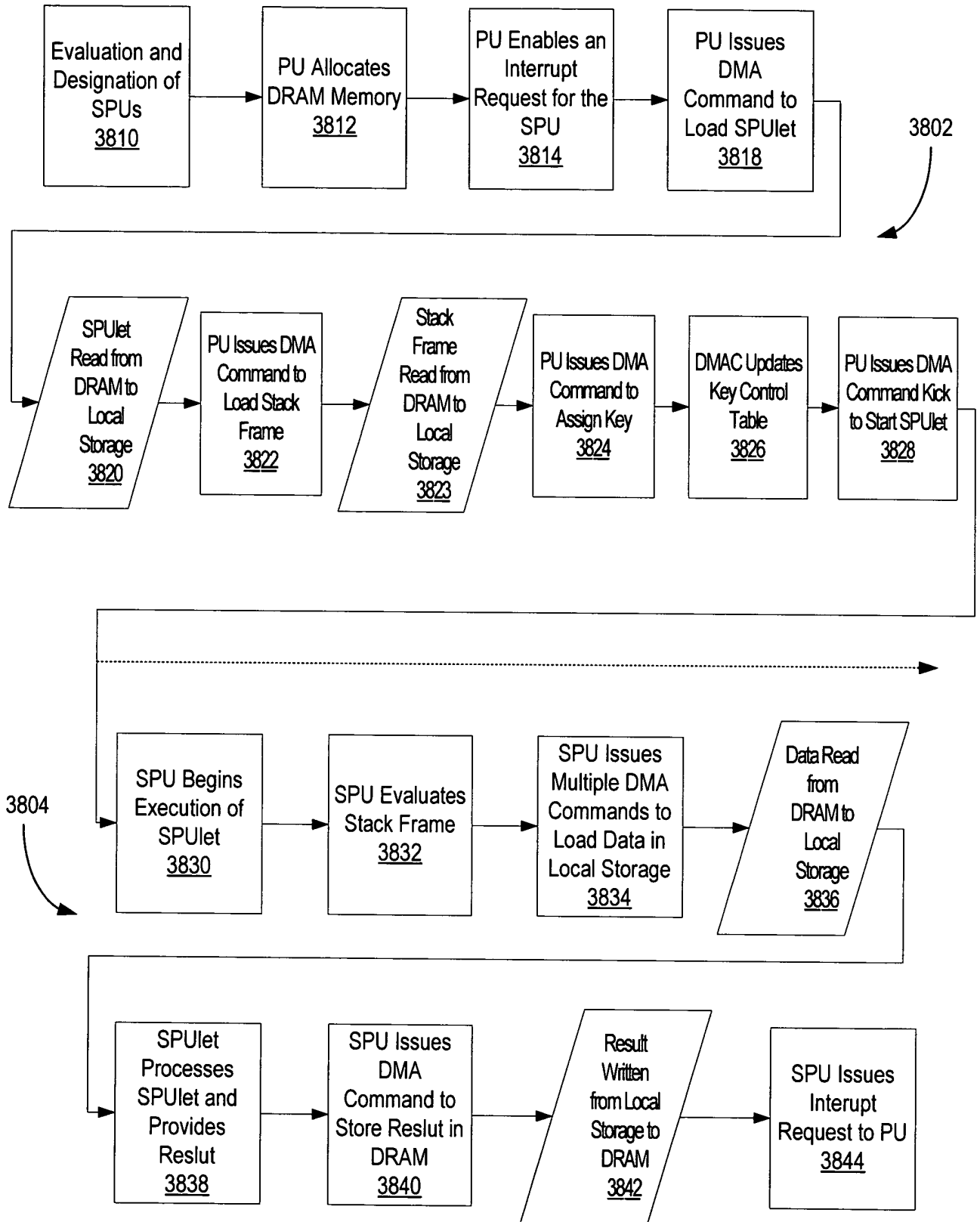
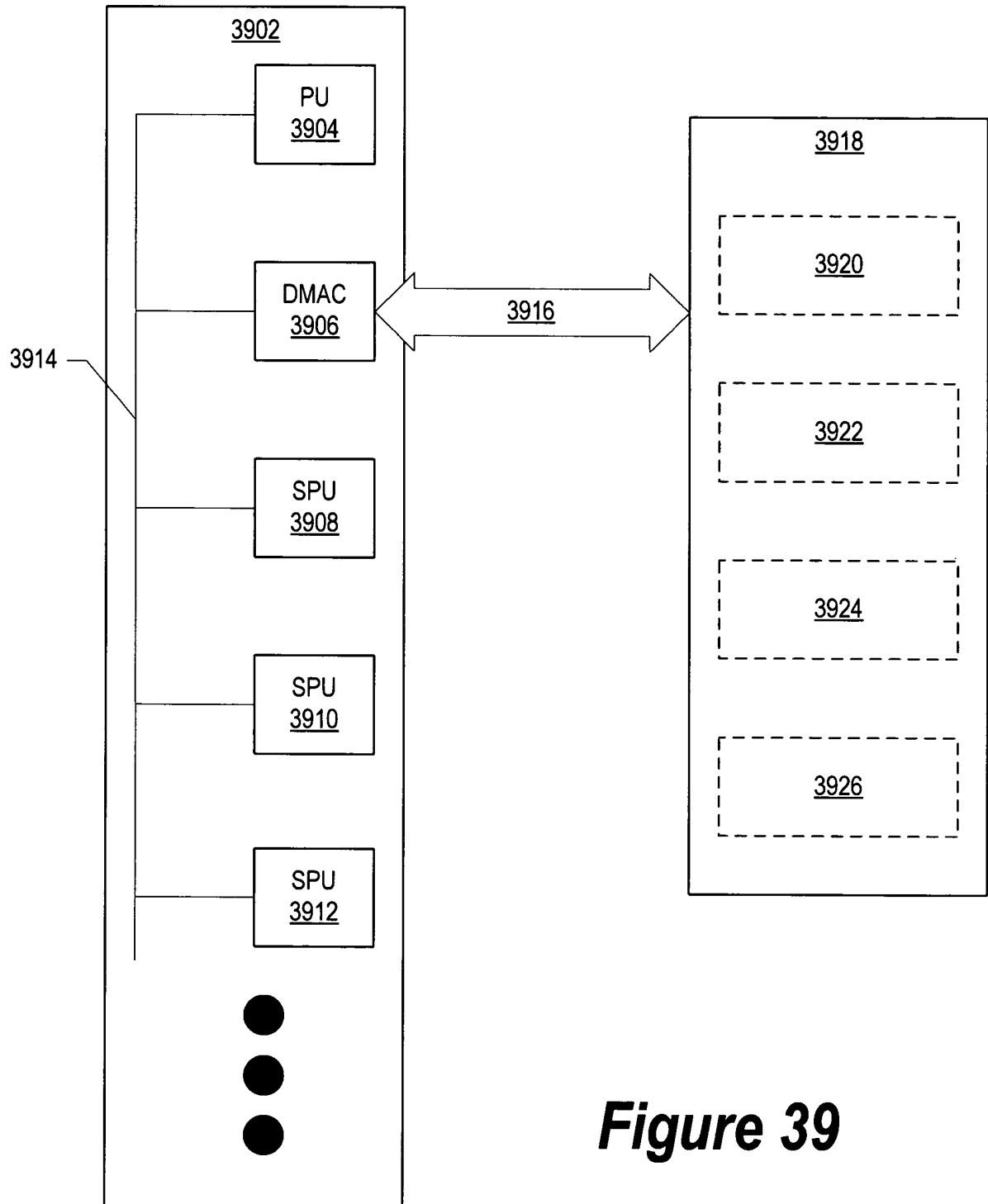


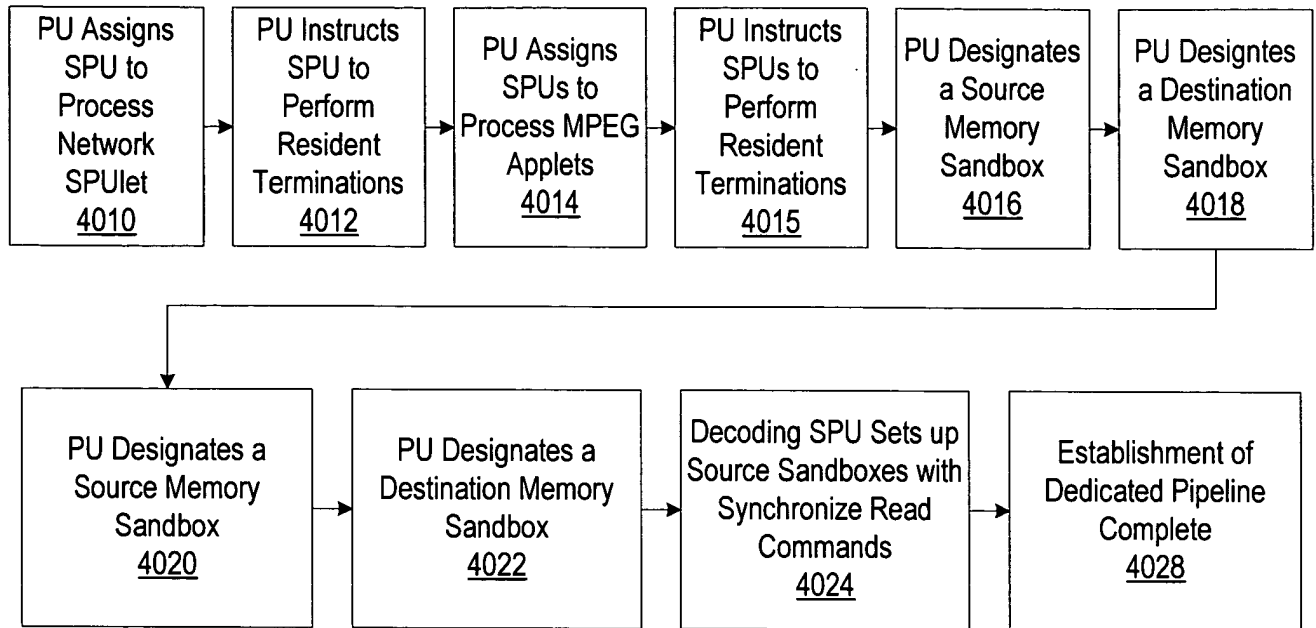
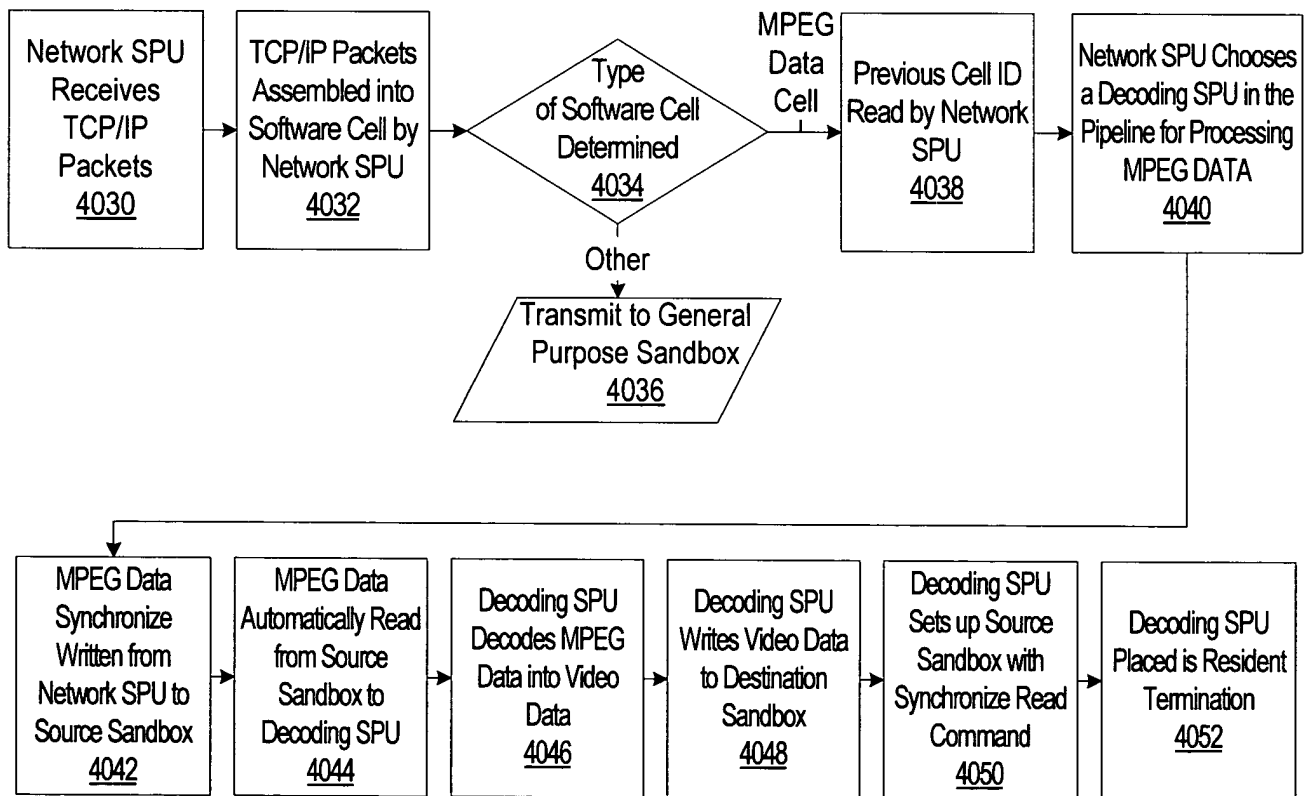
Figure 37

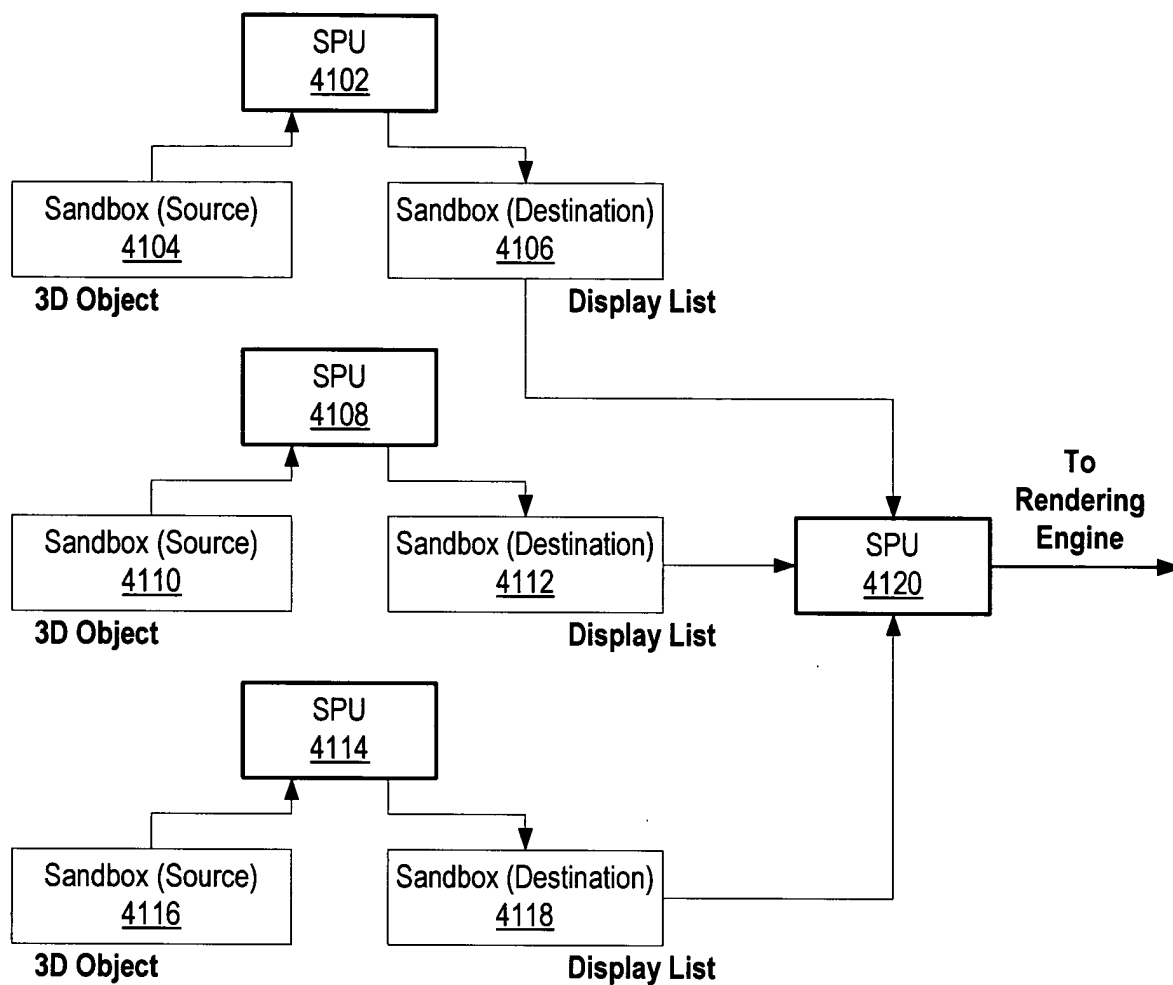
38 / 49

**Figure 38**

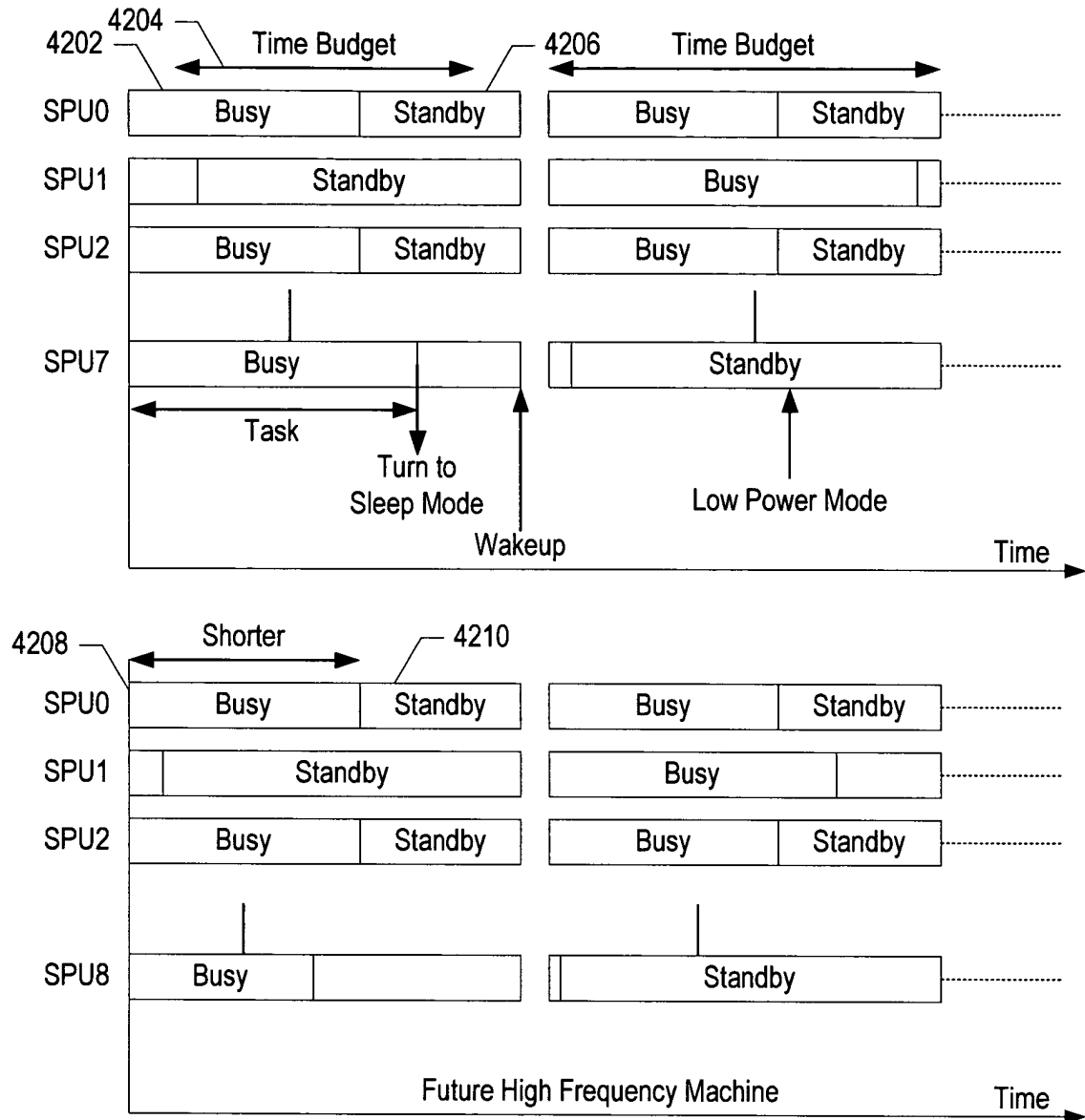
**Figure 39**

40 / 49

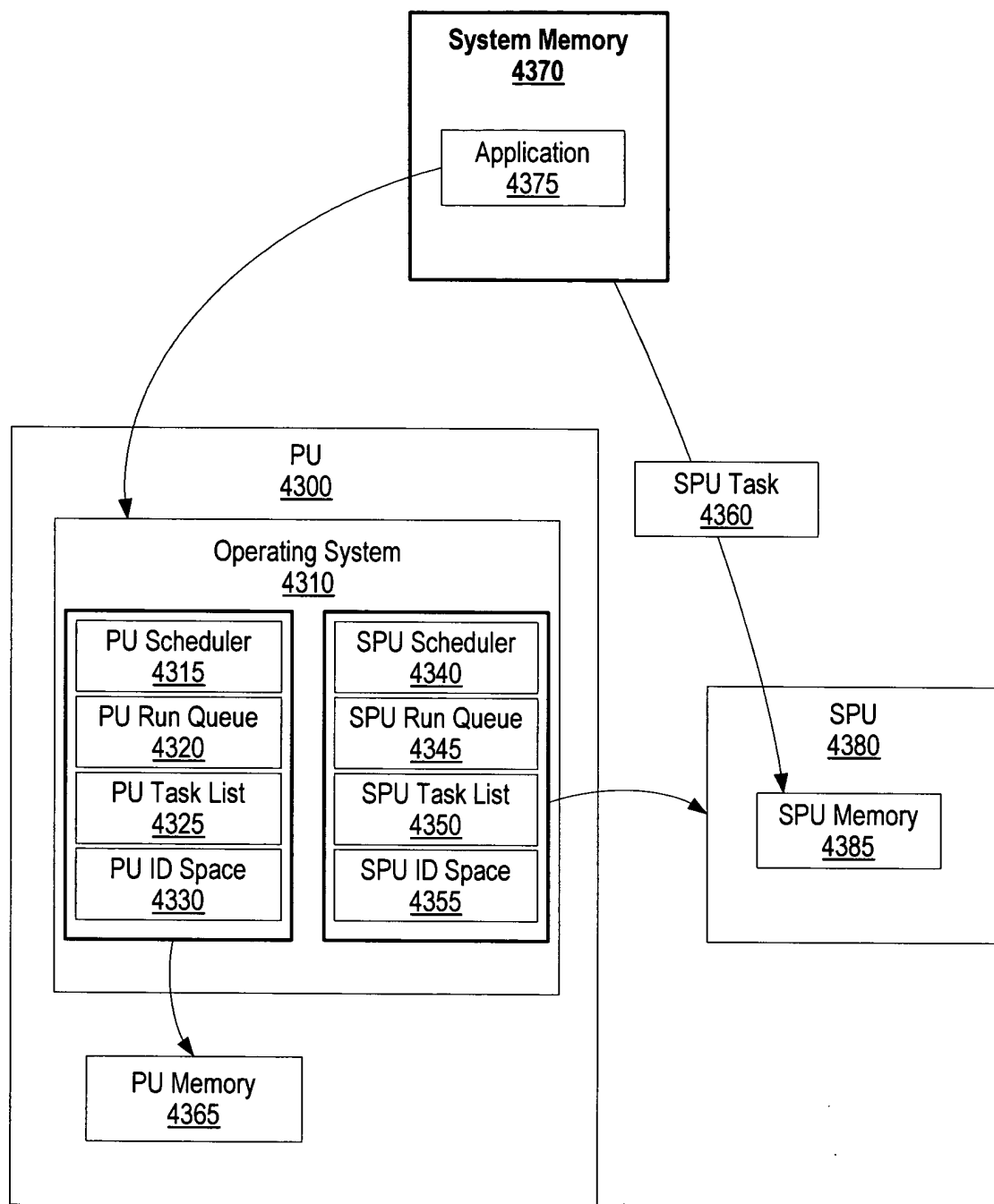
**Figure 40A****Figure 40B**

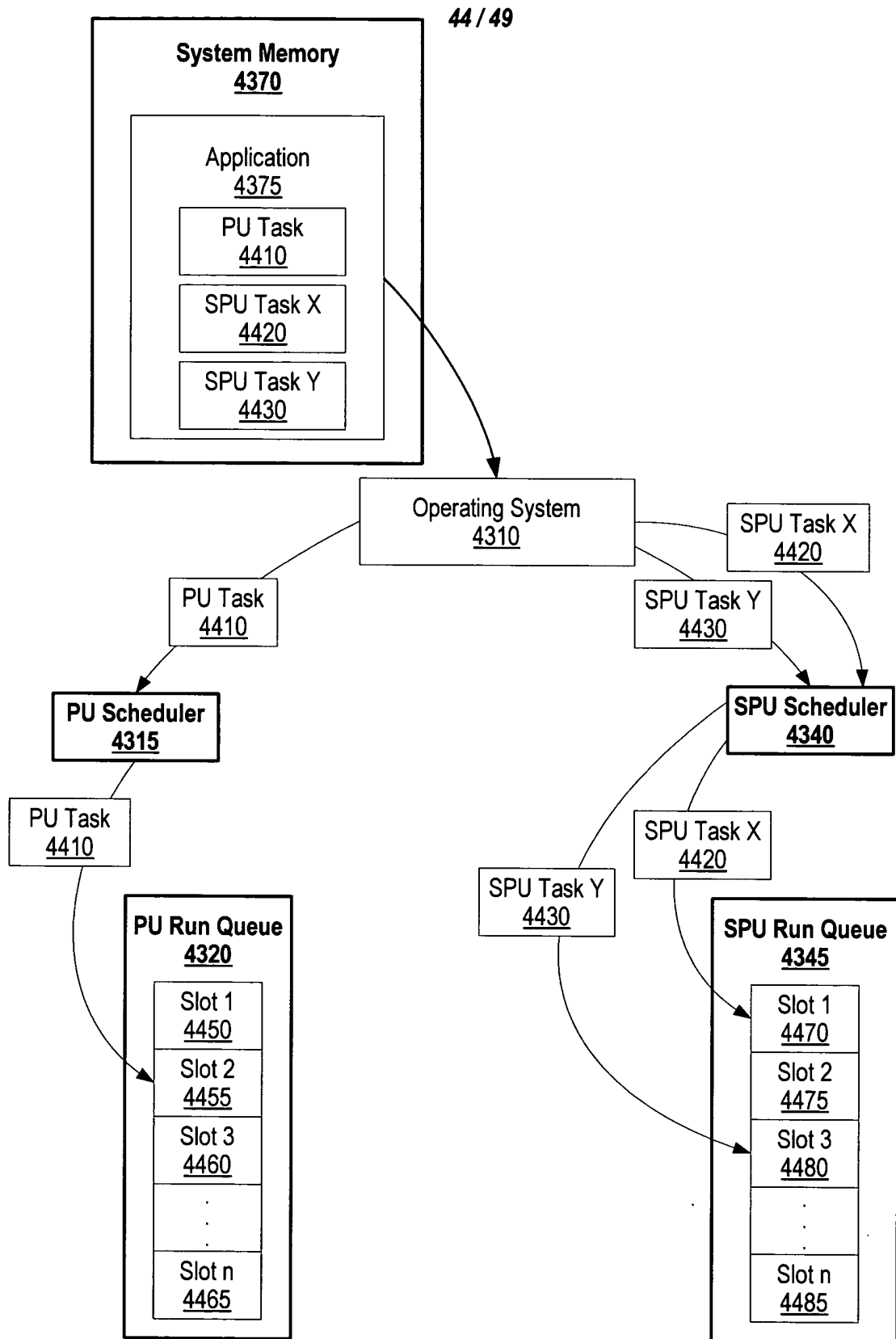
**Figure 41**

42 / 49

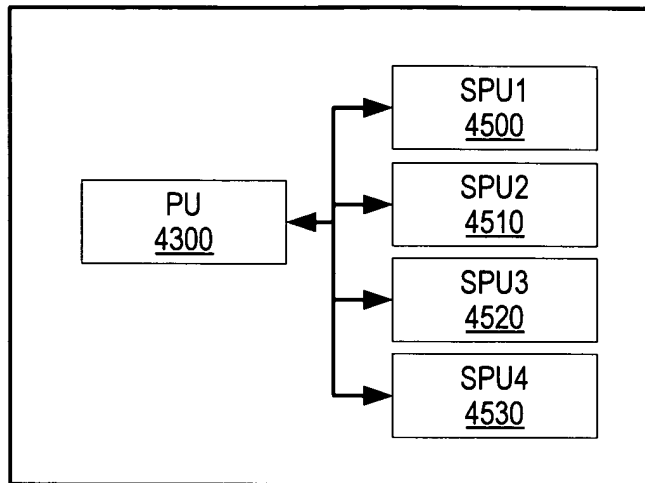
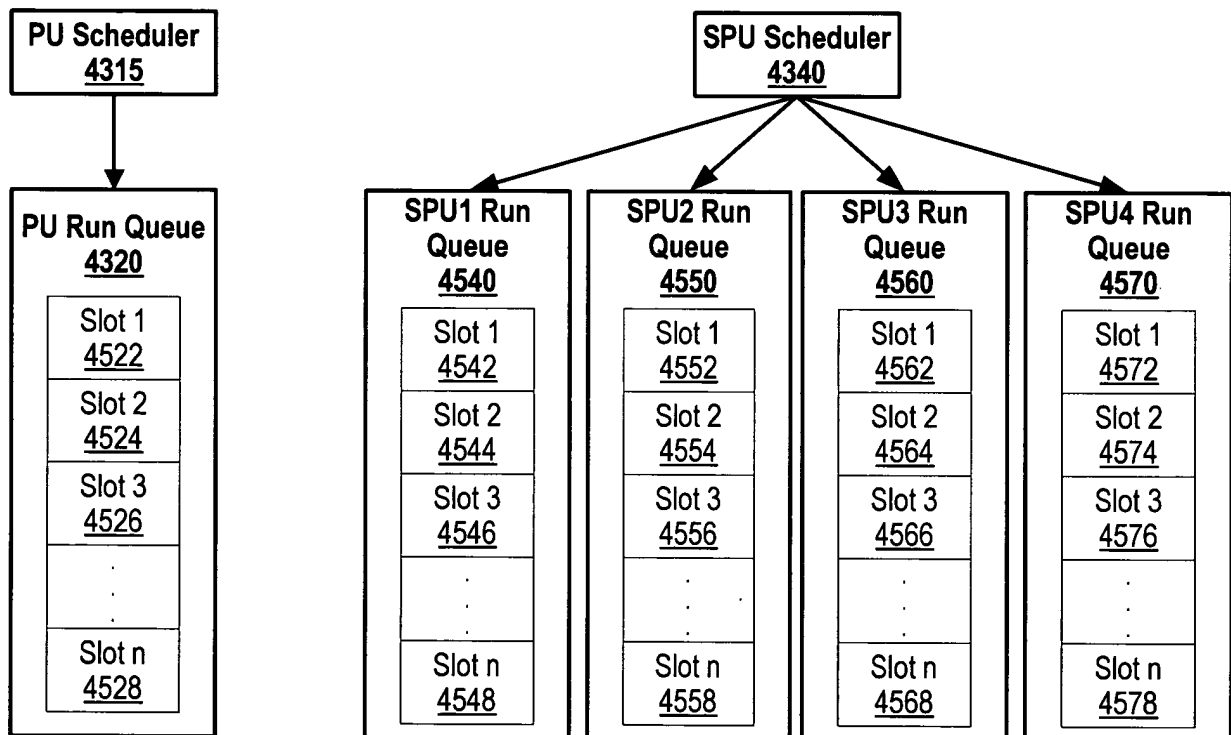
**Figure 42**

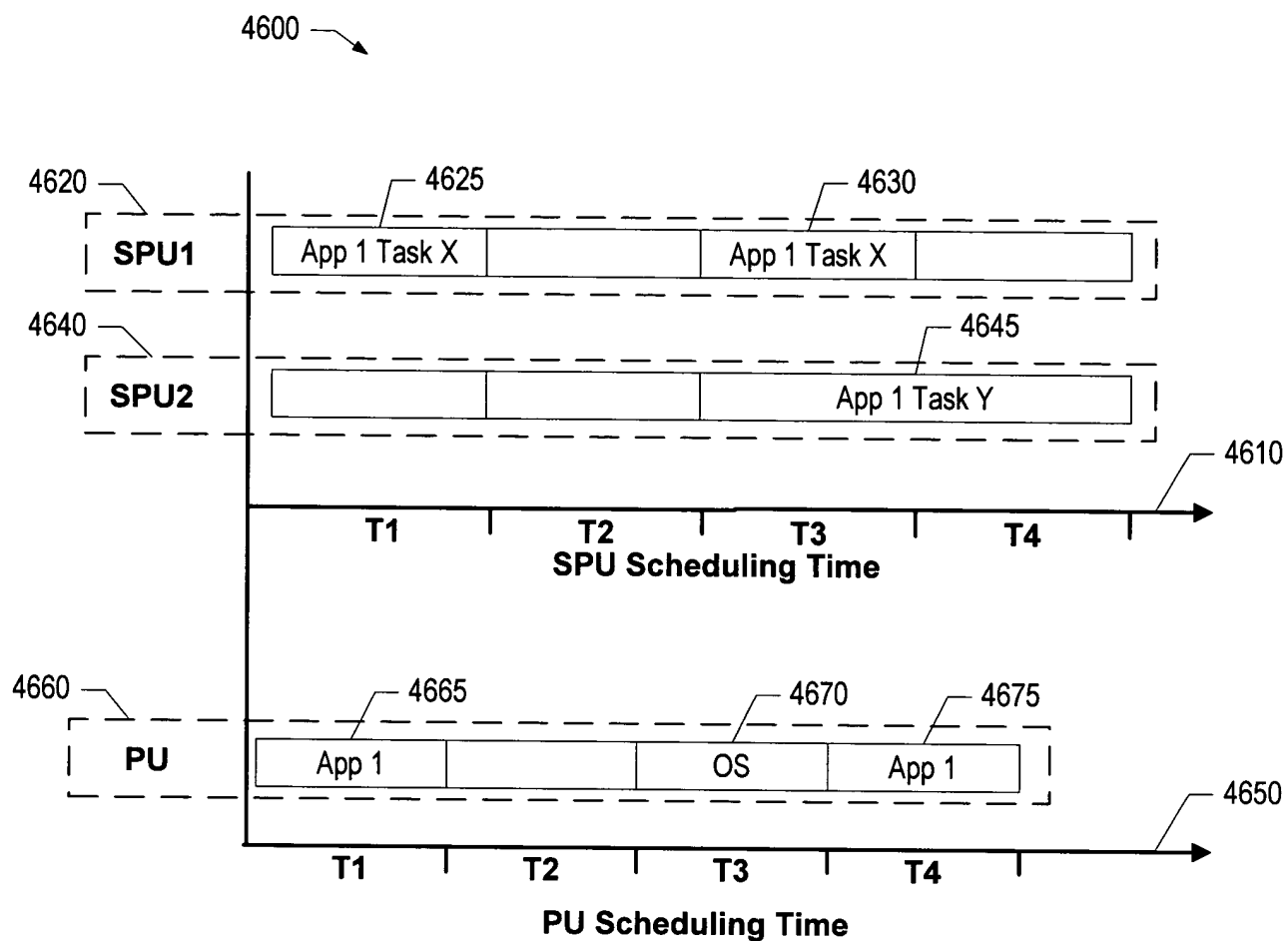
43 / 49

**Figure 43**

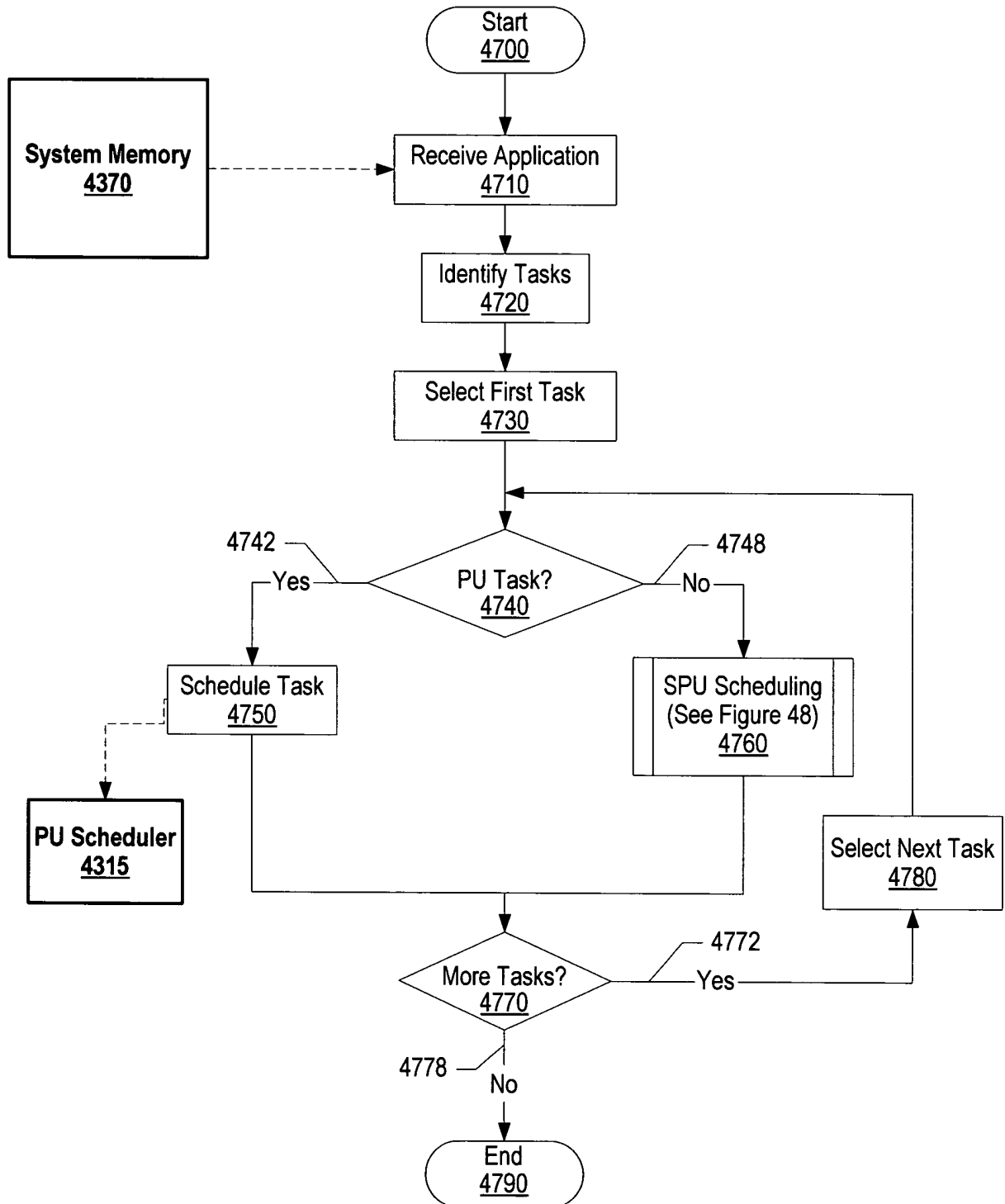
**Figure 44**

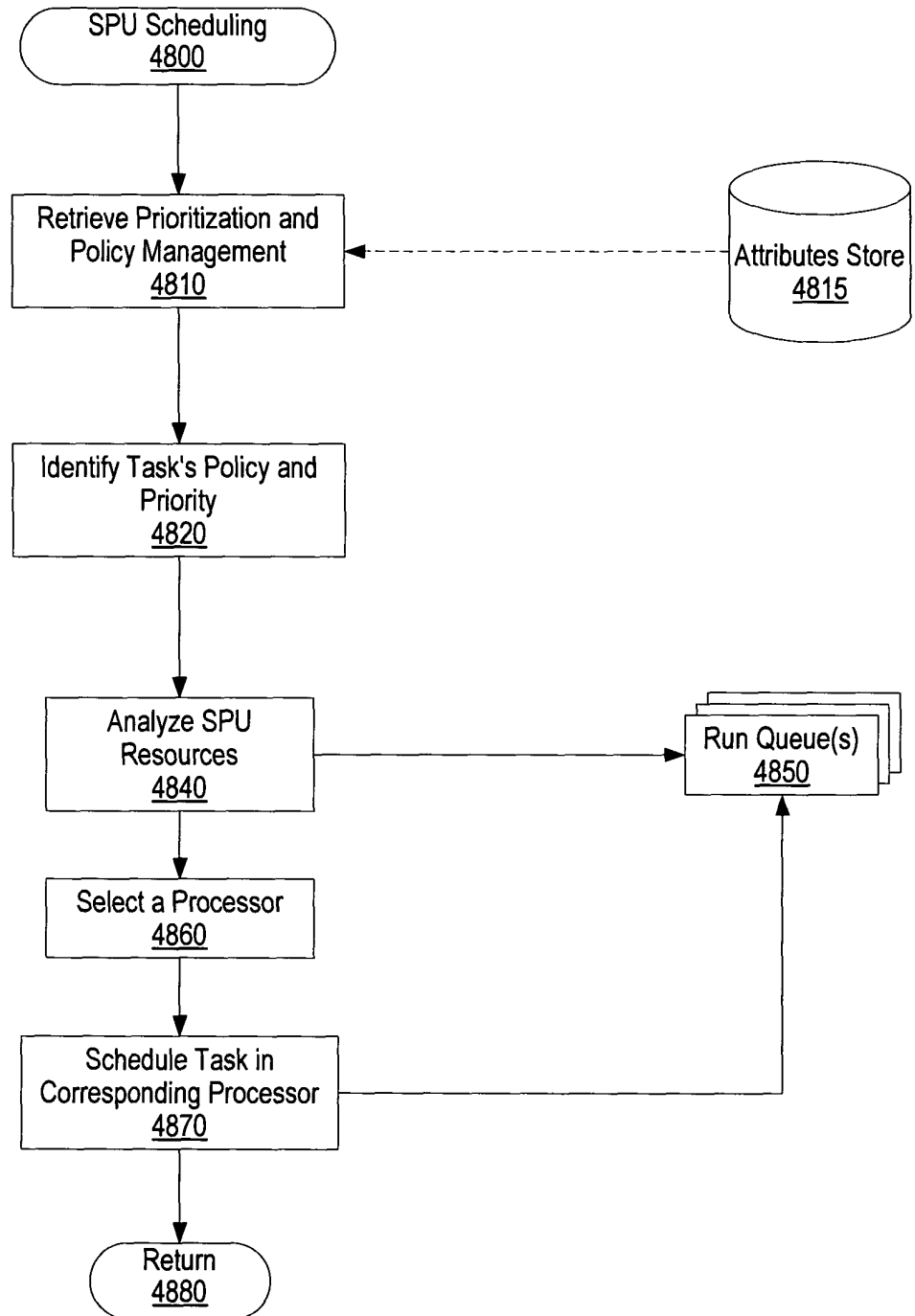
45 / 49

**Figure 45A****Figure 45B**

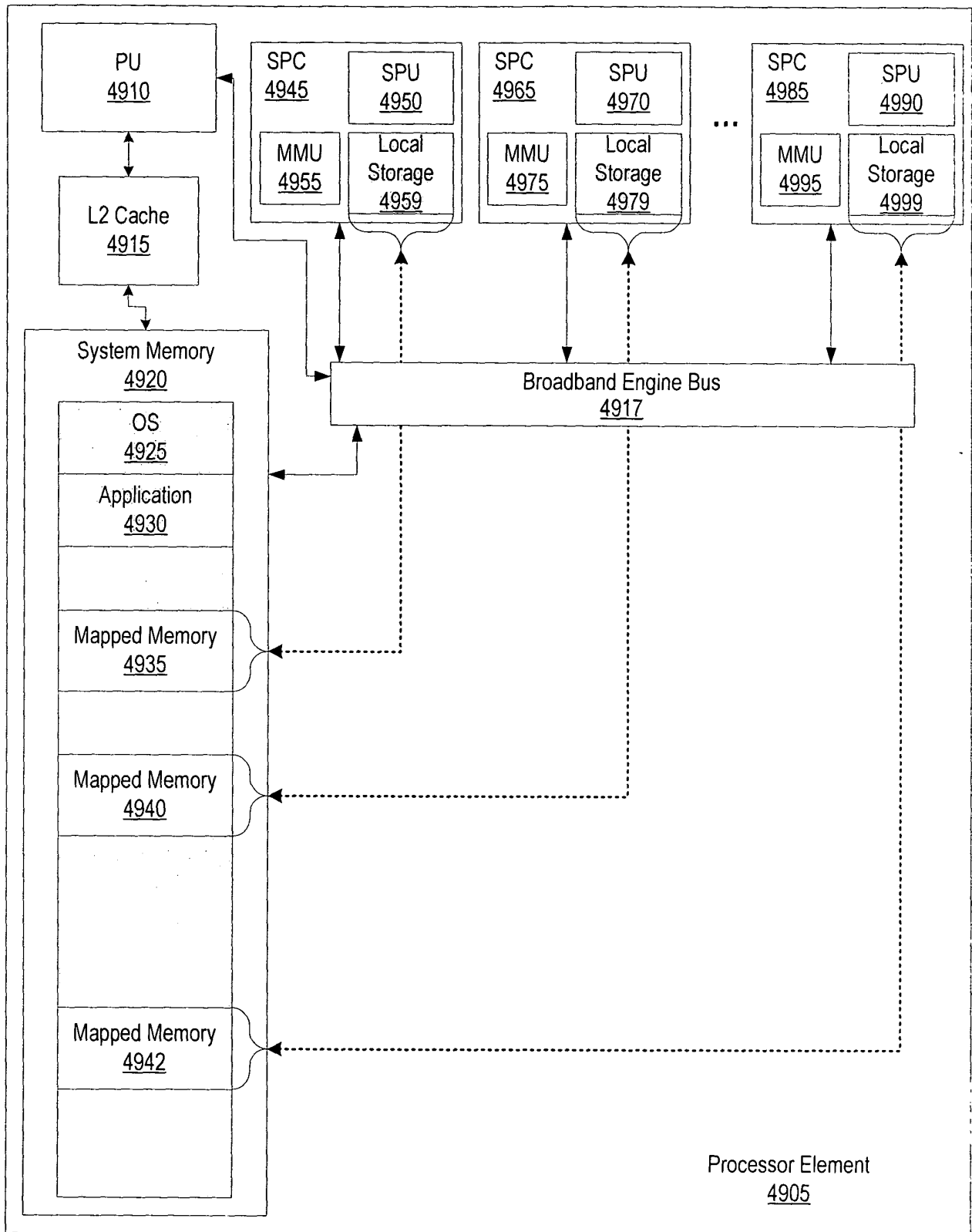
**Figure 46**

47 / 49

**Figure 47**

**Figure 48**

49 / 49

**Figure 49**